BHILAI INSTITUTE OF TECHNOLOGY, DURG

Revised Academic Calender Session - 2021-22

MBA 2nd Semester

DAYS	March 2022		April 2022		May 2022		June 2022	
Sun					1			
Mon					2	Feedback 1		
Tue	1	Maha Shiv Ratri			3	Id-Ul-Fitar		
Wed	2				4	Class Test 1 Result	1	Feedback 2
Thu	3				5		2	Class Test 2 Result
Fri	4		1		6		3	
Sat	5		2		7		4	
Sun	6		3		8		5	
Mon	7		4		9		6	
Tue	8		5		10		7	
Wed	9		6		11		8	
Thu	10		7		12		9	
Fri	11		8		13		10	
Sat	12		9		14		11	
Sun	13		10		15		12	
Mon	14		11		16	Buddha Purnima	13	
Tue	15		12		17		14	
Wed	16		13		18	Assignment 2	15	
Thu	17		14	Mahavir/ Ambedkar Jayanti	19		16	ESE Starts
Fri	18	Holi	15	Good Friday	20		17	ESE
Sat	19	Holi	16		21		18	ESE
Sun	20		17		22		19	
Mon	21	Commencement of Classes	18		23	Class Test 2	20	ESE
Tue	22		19	Assignment 1	24	Class Test 2	21	ESE
Wed	23		20		25	Class Test 2	22	ESE
Thu	24		21		26	Class Test 2	23	ESE
Fri	25		22	Class Test 1	27	Class Test 2	24	ESE End
Sat	26		23	Class Test 1	28	Class Test 2	25	Practical Exam
Sun	27		24		29		26	
Mon	28		25	Class Test 1	30	Class Test 2	27	
Tue	29		26	Class Test 1	31	Class Test 2	28	
Wed	30		27	Class Test 1			29	
Thu	31		28	Class Test 1			30	
Fri			29	Class Test 1				
Sat			30	Class Test 1				
Total Working days		20		24		24		26
Cumulative Working Days		20		44		68		94

* Classes / Programs to be conducted as per the COVID-19 Protocols.

Dr. M.K. Gupta
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Revised Academic Calender Session - 2021-22

B.Tech / M.Tech / MBA / MCA / B.VoC

DAYS	September 2021		October 2021		November 2021		December 2021		January 2022	
Mon					1					
Tue					2					
Wed	1	Commencement of Classes			3	Deepawali	1			
Thu	2				4	Deepawali	2			
Fri	3		1		5	Deepawali	3			
Sat	4		2	Gandhi Jayanti	6		4		1	
Sun	5		3		7		5		2	
Mon	6		4		8		6		3	ESE Starts (B.Tech / M.Tech / MCA / B.VoC)
Tue	7		5		9		7		4	ESE
Wed	8		6		10		8		5	ESE
Thu	9		7		11		9	Assignment 2	6	ESE
Fri	10	Ganesh Chaturthi	8		12		10		7	ESE Starts (MBA)
Sat	11		9		13		11		8	ESE
Sun	12		10		14		12		9	
Mon	13		11	Assignment 1	15		13	Class Test 2	10	ESE
Tue	14		12		16		14	Class Test 2	11	ESE
Wed	15		13	Maha Ashtami	17		15	Class Test 2	12	ESE
Thu	16		14	Maha Navami	18		16	Class Test 2	13	ESE
Fri	17		15	Dasahera	19	Guru Nanak Jayanti	17	Class Test 2	14	ESE
Sat	18		16		20		18	Guru Ghasidas Jayanti	15	ESE end
Sun	19		17		21		19		16	
Mon	20		18	Class Test 1	22		20	Feedback 2	17	Practical Exam
Tue	21		19	Eid-E-Milad	23		21	Result CT 2	18	
Wed	22		20	Class Test 1	24		22		19	
Thu	23		21	Class Test 1	25		23		20	
Fri	24		22	Class Test 1	26		24		21	
Sat	25		23	Class Test 1	27		25	Christmas	22	
Sun	26		24		28		26		23	
Mon	27		25	Feedback 1	29		27		24	
Tue	28		26	Result CT 1	30		28		25	
Wed	29		27				29		26	
Thu	30		28				30		27	
Fri			29				31		28	
Sat			30						29	
Sun			31						30	
									31	
Total Working days		25		21		22		25		25
Cumulative Working Days		25		46		68		93		118

* ESE for 5th, 7th Sem shall be as per the University Schedule.
* Classes / Programs to be conducted as per the COVID-19 Protocols.

Dr. M.K. Gupta
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY DURG (C.G.)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Revised Academic Calender Session - 2021-22

B.Tech / M.Tech / MBA / MCA / B.VoC

DAYS	September 2021		October 2021		November 2021		December 2021		January 2022	
Mon					1					
Tue					2					
Wed	1	Commencement of Classes			3	Deepawali	1			
Thu	2				4	Deepawali	2			
Fri	3		1		5	Deepawali	3			
Sat	4		2	Gandhi Jayanti	6		4		1	
Sun	5		3		7		5		2	
Mon	6		4		8		6		3	ESE Starts (B.Tech / M.Tech / MCA / B.VoC)
Tue	7		5		9		7		4	ESE
Wed	8		6		10		8		5	ESE
Thu	9		7		11		9	Assignment 2	6	ESE
Fri	10	Ganesh Chaturthi	8		12		10		7	ESE Starts (MBA)
Sat	11		9		13		11		8	ESE
Sun	12		10		14		12		9	
Mon	13		11	Assignment 1	15		13	Class Test 2	10	ESE
Tue	14		12		16		14	Class Test 2	11	ESE
Wed	15		13	Maha Ashtami	17		15	Class Test 2	12	ESE
Thu	16		14	Maha Navami	18		16	Class Test 2	13	ESE
Fri	17		15	Dasahera	19	Guru Nanak Jayanti	17	Class Test 2	14	ESE
Sat	18		16		20		18	Guru Ghasidas Jayanti	15	ESE end
Sun	19		17		21		19		16	
Mon	20		18	Class Test 1	22		20	Feedback 2	17	Practical Exam
Tue	21		19	Eid-E-Milad	23		21	Result CT 2	18	
Wed	22		20	Class Test 1	24		22		19	
Thu	23		21	Class Test 1	25		23		20	
Fri	24		22	Class Test 1	26		24		21	
Sat	25		23	Class Test 1	27		25	Christmas	22	
Sun	26		24		28		26		23	
Mon	27		25	Feedback 1	29		27		24	
Tue	28		26	Result CT 1	30		28		25	
Wed	29		27				29		26	
Thu	30		28				30		27	
Fri			29				31		28	
Sat			30						29	
Sun			31						30	
									31	
Total Working days		25		21		22		25		25
Cumulative Working Days		25		46		68		93		118

* ESE for 5th, 7th Sem shall be as per the University Schedule.
* Classes / Programs to be conducted as per the COVID-19 Protocols.

Dr. M.K. Gupta
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY DURG (C.G.)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Revised Academic Calender Session - 2021-22

B.Tech/ MBA/ MCA 4th Semester

DAYS	February 2022		March 2022		April 2022		May 2022		June 2022		July 2022	
Sun							1	Internship/ Vocational Training Starts				
Mon							2					
Tue	1		1	Maha Shiv Ratri			3	Id-Ul-Fitar				
Wed	2		2				4		1			
Thu	3	Commencement of Classes 4th Sem	3				5		2			
Fri	4		4		1		6		3		1	Class Test 2
Sat	5		5		2		7		4		2	Class Test 2
Sun	6		6		3		8		5		3	
Mon	7		7		4		9		6		4	Class Test 2
Tue	8		8		5		10		7		5	Class Test 2
Wed	9		9		6		11		8		6	Class Test 2
Thu	10		10		7		12		9		7	Feedback 2
Fri	11		11		8		13		10		8	
Sat	12		12		9		14		11		9	Result CT 2
Sun	13		13		10		15		12		10	
Mon	14		14	Assignment 1	11		16	Buddha Purnima	13		11	
Tue	15		15		12		17		14		12	
Wed	16		16		13		18		15	Internship/ Vocational Training End	13	
Thu	17		17		14	Mahavir/ Ambedkar Jayanti	19		16		14	
Fri	18		18	Holi	15	Good Friday	20		17		15	
Sat	19		19	Holi	16		21		18		16	
Sun	20		20		17		22		19		17	
Mon	21		21		18		23		20		18	
Tue	22		22	Class Test 1	19		24		21		19	
Wed	23		23	Class Test 1	20		25		22		20	ESE Starts
Thu	24		24	Class Test 1	21		26		23		21	ESE
Fri	25		25	Class Test 1	22		27		24		22	ESE
Sat	26		26	Class Test 1	23		28		25		23	ESE
Sun	27		27		24		29		26		24	
Mon	28		28	Feedback 1	25		30		27	Assignment 2	25	ESE
Tue			29	Result CT 1	26		31		28		26	ESE
Wed			30		27				29		27	ESE
Thu			31		28				30		28	ESE
Fri					29						29	ESE
Sat					30						30	ESE End
											31	
Total Working days		22		24		24		24		26		26
Cumulative Working Days		22		46		70		94		120		146

* Classes / Programs to be conducted as per the COVID-19 Protocols.

Dr. M.K. Gupta
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calender Session - 2020-21

MBA, I Semester

DAYS	November 2020	December 2020	January 2021	February 2021	March 2021
Mon				1	1 Class Test 2 Result
Tue		1		2	2
Wed		2		3	3
Thu		3		4	4
Fri		4	1	5	5
Sat		5	2	6	6
Sun	1	6	3	7	7
Mon	2	7 Induction Program	4 Class Test 1	8	8
Tue	3	8 Commencement of Classes	5 Class Test 1	9	9
Wed	4	9	6 Class Test 1	10	10
Thu	5	10	7 Class Test 1	11	11 Maha shiv Ratri
Fri	6	11	8 Class Test 1	12	12 ESE Starts
Sat	7	12	9 Class Test 1	13	13
Sun	8	13	10	14	14
Mon	9	14	11 Class Test 1	15	15
Tue	10	15	12 Class Test 1	16	16
Wed	11	16	13 Feedback 1	17	17
Thu	12 Deepawali	17	14 Makar Sankranti	18 Class Test 2	18
Fri	13 Deepawali	18 Guru Ghasidas Jayanti	15	19 Class Test 2	19
Sat	14 Deepawali	19	16	20 Class Test 2	20
Sun	15 Deepawali	20	17	21	21
Mon	16 Deepawali	21 Assignment 1	18 Result Class Test 1	22 Class Test 2	22
Tue	17	22	19	23 Class Test 2	23
Wed	18	23	20	24 Class Test 2	24
Thu	19	24	21	25 Class Test 2	25
Fri	20 BITCON-2020	25 Christmas	22	26 Class Test 2	26
Sat	21	26	23 Assignment 2	27 Feedback 2	27
Sun	22	27	24	28	28 Holi
Mon	23	28	25		29 Holi
Tue	24	29	26 Republic Day		30
Wed	25	30	27		31 ESE Ends
Thu	26	31	28		
Fri	27		29		
Sat	28		30		
Sun	29		31		
Mon	30 Guru Nanak Jayanti				
Total Working days		20	24	24	25
Cumulative Working Days		20	44	68	93

Note: Attendance will be displayed on the first working day of each month

Dr. M.K. Gupta
(Principal)

for Approval
14/12/20

Approved
OK.

VP
14/12/20

OFFICE OF PRINCIPAL
Bhilai Inst. of Tech; Durg
DIARY No 17296
DATE 9 DEC 2020

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calender Session - 2020-21

M.Tech/ MBA/ B.Voc 2nd Sem

DAYS	May 2021		June 2021		July 2021		August 2021	
Sun							1	
Mon							2	ESE Starts
Tue			1				3	ESE
Wed			2				4	ESE
Thu			3		1		5	ESE
Fri			4		2		6	ESE
Sat	1		5		3		7	ESE
Sun	2		6		4		8	
Mon	3		7		5		9	ESE
Tue	4		8		6		10	ESE
Wed	5		9		7		11	ESE
Thu	6		10		8		12	Practical Exam Starts
Fri	7		11		9		13	Practical Exam
Sat	8		12		10		14	Practical Exam End
Sun	9		13		11		15	Independence Day
Mon	10		14		12		16	Internship Starts
Tue	11		15		13		17	
Wed	12		16		14		18	
Thu	13		17		15		19	Muharram
Fri	14		18		16	Feedback 2	20	
Sat	15		19	Feedback 1	17	Assignment 2	21	
Sun	16		20		18		22	Raksha Bandhan
Mon	17		21	Assignment 1	19	Class Test 2	23	
Tue	18		22	Class Test 1	20	Class Test 2	24	
Wed	19		23	Class Test 1	21	Bakr Id	25	
Thu	20		24	Class Test 1	22	Class Test 2	26	
Fri	21		25	Class Test 1	23	Class Test 2	27	
Sat	22		26	Class Test 1	24	Class Test 2	28	
Sun	23		27		25		29	
Mon	24		28	Result of CT 1	26	Result of CT 2	30	Janmastami
Tue	25		29		27	PL Starts	31	
Wed	26		30		28	PL		
Thu	27				29	PL		
Fri	28				30	PL		
Sat	29				31	PL		
Sun	30							
Mon	31	2nd Semester Classes Restart						
Tue								
Total Working days		1		26		26		24
Cumulative Working Days		1		27		53		77

MBA 2nd Semester Classes commenced from 15th March 2021

29/5/21

Dr. M.K. Gupta
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calender MBA 1st Semester, ODD - 2019-20

DAYS	August 2019	September 2019	October 2019	November 2019	December 2019
Mon					
Tue			1		
Wed			2 Gandhi Jayanti		
Thu	1		3		
Fri	2 SESSION START 1 SEM		4 CT 01 Results	1	
Sat	3		5	2	
Sun	4	1	6	3	1
Mon	5	2 Ganesh Chaturthi	7 Mahanavmi	4	2
Tue	6	3	8 Dusshera	5	3
Wed	7	4	9	6	4
Thu	8	5	10	7	5 ECS
Fri	9	6	11	8	6 ECS
Sat	10	7	12	9	7 ECS
Sun	11	8	13	10	8
Mon	12 Id-Ul-Juha	9	14	11	9 ECS
Tue	13	10 Moharram	15	12 Gurunanak Jayanti	10 ECS
Wed	14	11	16	13	11 ECS
Thu	15 Independence Day/ Rakshabandhan	12	17	14	12 ECS
Fri	16	13	18	15	13 ECS
Sat	17	14	19	16	14 ECS
Sun	18	15	20	17	15
Mon	19	16	21	18 Class Test 02	16 ECS
Tue	20	17	22	19 Class Test 02	17 ECS
Wed	21	18	23	20 Class Test 02	18 Guru Ghasidas Jayanti
Thu	22	19	24	21 Class Test 02	19 ECS
Fri	23	20	25 Diwali	22 Class Test 02	20 ECS
Sat	24 Janmashtmi	21	26 Diwali	23	21 ECS
Sun	25	22	27 Diwali	24	22
Mon	26	23 Class Test 01	28 Diwali	25 Feedback 02	23 ECS
Tue	27	24 Class Test 01	29 Diwali	26 CT 02 Results	24 PE
Wed	28	25 Class Test 01	30 Assignment 02	27	25 Christmas
Thu	29	26 Class Test 01	31 Parent Meet	28	26 PE
Fri	30 Assignment 01	27 Class Test 01		29	27 PE
Sat	31 Parent Meet	28		30	28 PE
Sun		29			29
Mon		30 Feedback 01			30
Tue					31
Total Working days	22	19	20	20	3
Cumulative Working Days	22	41	61	81	84

Note: Attendance will be displayed on the first working day of each month

Dr. Arun Arora
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calender MBA 3rd Semester, ODD - 2019-20

DAYS	August 2019	September 2019	October 2019	November 2019	December 2019	January 2020
Mon						
Tue			1			
Wed			2 Gandhi Jayanti			1 ECS
Thu	1		3			2 ECS
Fri	2		4 CT 01 Results	1		3 ECS
Sat	3		5	2		4 PE
Sun	4	1	6	3	1	5
Mon	5	2 Ganesh Chaturthi	7 Mahanavmi	4	2	6 PE
Tue	6	3	8 Dusshera	5	3	7 PE
Wed	7	4	9	6	4 ECS	8 PE
Thu	8 SESSION START 3 SEM	5	10	7	5 ECS	
Fri	9	6	11	8	6 ECS	
Sat	10	7	12	9	7 ECS	
Sun	11	8	13	10	8	
Mon	12 Id-Ul-Juha	9	14	11	9 ECS	
Tue	13	10 Moharram	15	12 Gurunanak Jayanti	10 ECS	
Wed	14	11	16	13	11 ECS	
Thu	15 Independence Day/ Rakshabandhan	12	17	14	12 ECS	
Fri	16	13	18	15	13 ECS	
Sat	17	14	19	16	14 ECS	
Sun	18	15	20	17	15	
Mon	19	16	21	18 Class Test 02	16 ECS	
Tue	20	17	22	19 Class Test 02	17 ECS	
Wed	21	18	23	20 Class Test 02	18 Guru Ghasidas Jayanti	
Thu	22	19	24	21 Class Test 02	19 ECS	
Fri	23	20	25 Diwali	22 Class Test 02	20 ECS	
Sat	24 Janmashtmi	21	26 Diwali	23	21 ECS	
Sun	25	22	27 Diwali	24	22	
Mon	26	23 Class Test 01	28 Diwali	25 Feedback 02	23 ECS	
Tue	27	24 Class Test 01	29 Diwali	26 CT 02 Results	24 ECS	
Wed	28	25 Class Test 01	30 Assignment 02	27	25 Christmas	
Thu	29	26 Class Test 01	31 Parent Meet	28	26 ECS	
Fri	30 Assignment 01	27 Class Test 01		29	27 ECS	
Sat	31 Parent Meet	28		30	28 ECS	
Sun		29			29	
Mon		30 Feedback 01			30 ECS	
Tue					31 ECS	
Total Working days	17	19	20	20	2	
Cumulative Working Days	17	36	56	76	78	

Note: Attendance will be displayed on the first working day of each month

Dr. Arun Arora
(Principal)

Department of Management

TentativeTime Table for MBA I & III Semester – 2019

Day	Sem. & Sec.	I	II	III	IV	1:20-2:10	V	VI	VII
		10:00 – 10:50	10:50-11:40	11:40-12:30	12:30-1.20		2:10-:3:00	3:00-3:50	3:50-4:40
MONDAY	I A	AFM/DSS	ME/SK	CAM LAB/ SS		LUNCH BREAK	BL/DMS	MCP/AP	CCA/AC
	I B	ET/AP	BL/DJG	MCP/DSS	AFM/DMS		ME/KKD	QTM/DKG	MSD/SM
	III A	CB/DAC	SM/DSW SM/AC CRM/DSG HRPD/SP	ITM/AP	OM/DSW		TAX/SK FO/ST CM/SP MTD/SM SE/ CR	SAPM/DUS HRPD/DAC RDBMS/CC	MR/ST ASP/DJG CM/DSS MTD/KKD
	III B	ODC/KKD		OM/DKG	ITM/AC				
TUESDAY	I A	ME/SK	QTM/DSW	BL/DMS	MSD/DUS		BS/SP	ET/DAC	AFM/DSS
	I B	MCP/DSS	ME/KKD	MSD/SM	QTM/DKG		ET/AP	BS/AC	AFM/DMS
	III A	ODC/DMS	SAPM/DUS HRPD/DAC RDBMS/CC	Research Lab/ ST			MR/ST ASP/DJG CM/DSS MTD/KKD	OM/DSW	TAX/SK FO/ST CM/SP MTD/SM SE/ CR
				NSTL/DKG	Summer Training/DJG				
	III B	CB/DJG		ITM/AC	ODC/KKD			OM/DKG	
WEDNESDAY	I A	MSD/DUS	ME/SK	AFM/DSS	BS/SP		MCP/AP	QTM/DSW	ET/DAC
	I B	MSD/SM	ME/KKD	BL/DJG	AFM/DMS		BS/AC	QTM/DKG	MCP/DSS
	III A	ITM/AP	SM/DDSW SM/AC CRM/DSG HRPD/SP	CB/DAC	OM/DSW		SAPM/DUS HRPD/DAC RDBMS/CC	TAX/SK FO/ST CM/SP MTD/SM SE/ CR	ODC/DMS
	III B	OM/DKG		NSTL/AP	Summer Training/DKG				CB/DJG
				Research Lab/ ST					
THRDUSDAY	I A	BS/SP	MSD/DUS	ET/DAC	BL/DMS		MCP/AP	ME/SK	QTM/DSW
	I B	QTM/DKG	ME/KKD	CAM LAB/ SS			CCA/DUS	AFM/DMS	BS/AC
	III A	OM/DSW	ODC/DMS	ITM/AP	TAX/SK FO/ST CM/SP MTD/SM SE/ CR		SM/DDSW SM/AC CRM/DSG HRPD/SP	MR/ST ASP/DJG CM/DSS MTD/KKD	SAPM/DUS HRPD/DAC RDBMS/CC
	III B	ITM/AC	CB/DJG	ODC/KKD					
FRIDAY	I A	BS/SP	AFM/DSS	MCP/AP	QTM/DSW		ET/DAC	GCL/DUS	BL/DMS
	I B	ET/AP	QTM/DKG	ME/KKD	BL/DJG		BS/AC	GCL/SM	MCP/DSS
	III A	CB/DAC	OM/DSW	SAPM/DUS HRPD/DAC RDBMS/CC	TAX/SK FO/ST CM/SP MTD/SM SE/ CR		MR/ST ASP/DJG CM/DSS MTD/KKD	ITM/AP	SM/DDSW SM/AC CRM/DSG HRPD/SP
	III B	ITM/AC	CB/DJG					OM/DKG	

Day	Sec.	10:00 – 10:50	10:50-11:40	11:40-12:30	12:30-1.20	1.20-2.10
SATURDAY	I A	ME/SK	AFM/DSS	MSD/DUS	QTM/DSW	LIBRARY
	I B	BL/DJG	ET/AP	AFM/DMS	MSD/SM	
	III A	ODC/DMS	SM/DDSW SM/AC CRM/DSG HRPD/SP	CB/DAC	MR/ST ASP/DJG CM/DSS MTD/KKD	
	III B	ODC/KKD		OM/DKG		

Note: Any doubt/discrepencies, contact Head.

Time Table Coordinator

Head

BHILAI INSTITUTE OF TECHNOLOGY, DURG C.G.

Academic Calender MBA IV Semester(Jan to June - 2020)

DAYS	JAN 2020	FEB 2020	MAR 2020	APRIL 2020	MAY 2020	JUNE 2020
Mon						1 ESE PRACTICAL
Tue						2 ESE PRACTICAL
Wed	1			1		3 ESE PRACTICAL
Thu	2			2 (Ram Navami)		4 ESE PRACTICAL
Fri	3			3 (Content Beyond Syllabus)	1 ESE	5 ESE PRACTICAL
Sat	4	1		4	2 ESE	6 ESE PRACTICAL
Sun	5	2	1	5	3	7
Mon	6	3	2	6 (Mahaveer Jayanti)	4 ESE	8 ESE PRACTICAL
Tue	7	4	3	7 CT-2	5 ESE	9 ESE PRACTICAL
Wed	8	5 (YMA Activity)	4 (Declaration of CT-1 Results)	8 CT-2	6 ESE	10
Thu	9	6	5	9 CT-2	7 (Budh Purnima)	11
Fri	10	7	6	10 (Good Friday)	8 ESE	12
Sat	11	8	7	11 CT-2	9 ESE	13
Sun	12	9	8	12	10	14
Mon	13	10	9	13 CT-2	11 ESE	15
Tue	14	11 (Guest Lecture)	10 (Holi)	14 (Ambedkar Jayanti)	12 ESE	16
Wed	15 (Makar Sankranti)	12	11 (Holi)	15(2nd-Feedback)	13 ESE	17
Thu	16 (Commencement of 4th sem)	13 (Content Beyond Syllabus)	12	16 PL START	14 ESE	18
Fri	17	14	13	17	15 ESE	19
Sat	18	15	14	18 Declaration Of CT-2 Results	16 ESE	20
Sun	19	16	15	19	17	21
Mon	20	17	16(1st-Feedback)	20	18 ESE	22
Tue	21	18	17	21	19 ESE	23
Wed	22 (Content Beyond Syllabus)	19 (OJAS 2020)	18 (Content Beyond Syllabus)	22	20 ESE	24
Thu	23	20 (OJAS 2020)	19	23	21 ESE	25
Fri	24	21 (Mahashivratri)	20	24	22 ESE	26
Sat	25	22	21	25	23 ESE	27
Sun	26 (Republic Day)	23	22	26	24	28
Mon	27	24 (CT-1)	23	27	25 (Id-Ul-Fitr)	29
Tue	28	25 (CT-1)	24 (Industrial Visit-YMA)	28 ESE	26 ESE	30
Wed	29 (Guest Lecture)	26 (CT-1)	25	29 ESE	27 ESE	
Thu	30	27 (CT-1)	26	30 ESE	28 ESE	
Fri	31	28 (CT-1)	27		29 ESE PRACTICAL	
Sat		29	28		30 ESE PRACTICAL	
Sun			29 (NPTEL Exam)		31	
Mon			30			
Tue			31			
TWD 4th sem	14	23	24	9		
CWD 4th sem	14	37	61	70		

Note : Attendance will be displayed on the first working-day of each month

Head

Principal
BIT Durg

BHILAI INSTITUTE OF TECHNOLOGY, DURG C.G.

Academic Calender MBA II Semester(Jan to June - 2020)

DAYS	JAN 2020	FEB 2020	MAR 2020	APRIL 2020	MAY 2020	JUNE 2020
Mon						1
Tue						2
Wed	1			1		3
Thu	2			2 (Ram Navami)		4
Fri	3			3 (Content Beyond Syllabus)	1 ESE	5
Sat	4	1		4	2 ESE	6
Sun	5	2	1	5	3	7
Mon	6 (Commencement of 2nd sem)	3	2	6 (Mahaveer Jayanti)	4 ESE	8
Tue	7	4	3	7 CT-2	5 ESE	9
Wed	8	5 (YMA Activity)	4 (Declaration of CT-1 Results)	8 CT-2	6 ESE	10
Thu	9	6	5	9 CT-2	7 (Budh Purnima)	11
Fri	10	7	6	10 (Good Friday)	8 ESE	12
Sat	11	8	7	11 CT-2	9 ESE	13
Sun	12	9	8	12	10	14
Mon	13	10	9	13 CT-2	11 ESE	15
Tue	14	11 (Guest Lecture)	10 (Holi)	14 (Ambedkar Jayanti)	12 ESE	16
Wed	15 (Makar Sankranti)	12	11 (Holi)	15(2nd-Feedback)	13 ESE	17
Thu	16	13 (Content Beyond Syllabus)	12	16 PL START	14 ESE	18
Fri	17	14	13	17	15 ESE	19
Sat	18	15	14	18 Declaration of CT 2 Results	16 ESE PRACTICAL	20
Sun	19	16	15	19	17	21
Mon	20	17	16(1st-Feedback)	20	18 ESE PRACTICAL	22
Tue	21	18	17	21	19 ESE	23
Wed	22 (Content Beyond Syllabus)	19 (OJAS 2020)	18 (Content Beyond Syllabus)	22	20 ESE PRACTICAL	24
Thu	23	20 (OJAS 2020)	19	23	21 ESE PRACTICAL	25
Fri	24	21 (Mahashivratri)	20	24	22 ESE	26
Sat	25	22	21	25	23 ESE PRACTICAL	27
Sun	26 (Republic Day)	23	22	26	24	28
Mon	27	24 (CT-1)	23	27 ESE	25 (Id-Ul-Fitr)	29
Tue	28	25 (CT-1)	24 (Industrial Visit-YMA)	28 ESE	26 ESE PRACTICAL	30
Wed	29 (Guest Lecture)	26 (CT-1)	25	29 ESE	27	
Thu	30	27 (CT-1)	26	30 ESE	28	
Fri	31	28 (CT-1)	27		29	
Sat		29	28		30	
Sun			29 (NPTEL Exam)		31	
Mon			30			
Tue			31			
TWD 2nd sem	22	23	24	9		
CWD 2nd sem	22	45	69	78		

Note : Attendance will be displayed on the first working-day of each month

Head

Principal
BIT Durg

Department of Management

Tentative Time Table for MBA II & IV Semester – 2020

Day	Sec.	I	II	III	IV	1:20-2:10	V	VI	VII
		10:00 – 10:50	10:50-11:40	11:40-12:30	12:30-1.20		2:10-:3:00	3:00-3:50	3:50-4:40
MONDAY	II A	ED/SM	HRM/AP	RM/SK	MIS/AC		MM/SP	AFM/DSS	Mat.M/DSW
	II B	Mat.M/MS	MIS/AC	AFM/DUS	ED/AP		MM/DJG	RM/DKG	HRM/SP
	IV A	EDM/DKG	RETAIL/DJG ISMM/SG EI/DSS	CS/KKD	PBM/DJG IBM/DAC IR/SP SHRM/DSS		PPA/ST B&I//SK EI/KKD DM/DAR	IFM/DUS MWC/SG BVA/ST IR/DAC SHRM/SM ERP/RP	T&T/AC Banking/MS Event/AP SME/KKD
	IV B	CS/DUS		EDM/DSW					
TUESDAY	II A	RM/SK	AFM/DSS	Mat.M/DSW	ED/SM		MIS/AC	MM/SP	HRM/AP
	II B	MM/DJG	RM/DKG	R.LAB/SK			ED/AP	POM/DSW	AFM/DUS
	IV A	CS/KKD	IFM/DUS MWC/SG BVA/ST IR/DAC SHRM/SM ERP/RP	T&T/AC Banking/MS Event/AP SME/KKD	PBM/DJG IBM/DAC IR/SP SHRM/DSS		LIBRARY	RETAIL/DJG ISMM/SG EI/DSS	EDM/DKG
	IV B	EDM/DSW					CS/DUS		*MAJOR REPORT*
WEDNESDAY	II A	POM/DKG	HRM/AP	R.LAB/ DJG		LUNCH BREAK	RM/SK	MIS/AC	AFM/DSS
	II B	MIS/AC	Mat.M/MS	POM/DSW	HRM/SP		RM/DKG	ED/AP	LIBRARY
	IV A	PBM/DJG IBM/DAC IR/SP SHRM/DSS	PPA/ST B&I//SK EI/KKD DM/DAR	EDM/DKG	T&T/AC Banking/MS Event/AP SME/KKD		RETAIL/DJG ISMM/SG EI/DSS	CS/KKD	IFM/DUS MWC/SG BVA/ST IR/DAC SHRM/SM ERP/RP
	IV B			CS/DUS				EDM/DSW	
THRUSDAY	II A	HRM/AP	RM/SK	AFM/DSS	R.Report/DAC		POM/DKG	ED/SM	LIBRARY
	II B	Mat.M/MS	MM/DJG	HRM/SP	MIS/AC		AFM/DUS	R.Report/ SG	RM/DKG
	IV A	CS/KKD	IFM/DUS MWC/SG BVA/ST IR/DAC SHRM/SM ERP/RP	R.LAB/DSW			PPA/ST B&I//SK EI/KKD DM/DAR	PBM/DJG IBM/DAC IR/SP SHRM/DSS	T&T/AC Banking/MS Event/AP SME/KKD
	IV B	EDM/DSW		LIBRARY	CS/DUS				
FRIDAY	II A	RM/SK	Mat.M/DSW	ED/SM	MM/SP		HRM/AP	POM/DKG	AFM/DSS
	II B	RM/DKG	AFM/DUS	Mat.M/MS	POM/DSW		MM/DJG	HRM/SP	MIS/AC
	IV A	PBM/DJG IBM/DAC IR/SP SHRM/DSS	CS/KKD	EDM/DKG	RETAIL/DJG ISMM/SG EI/DSS		PPA/ST B&I//SK EI/KKD DM/DAR	IFM/DUS MWC/SG BVA/ST IR/DAC SHRM/SM ERP/RP	*MAJOR REPORT*
	IV B		R.LAB/ST						EDM/DSW

Day	Sec.	10.00-10.50	10.50-11.40	11.40-12.30	12:30-1:20
SATURDAY	II A	MIS/AC	Mat.M/DSW	POM/DKG	MM/SP
	II B	ED/AP	HRM/SP	AFM/DUS	POM/DSW
	IV A	EDM/DKG	PPA/ST B&I//SK EI/KKD DM/DAR	RETAIL/DJG ISMM/SG EI/DSS	T&T/AC Banking/MS Event/AP SME/KKD
	IV B	CS/DUS			

Note: Any doubt/discrepencies, contact Head.

Time Table Coordinator
Saurabh Tomar
Dr Judith Gomes Nagar

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calender MBA- I Semester, ODD - 2018-19

DAYS	August 2018	September 2018	October 2018	November 2018	December 2018	January 2019
Mon			1 FB-1			
Tue			2 GANDHI JAYANTI			1 PE
Wed	1		3 CTR-1			2 PE
Thu	2		4 PTM-1	1		3
Fri	3 I&O		5	2		4
Sat	4 I&O	1	6	3	1 PL	5
Sun	5	2	7	4	2 PL	6
Mon	6 I&O	3 JANMASHATAMI	8	5 DIWALI	3 PL	7
Tue	7	4	9	6 DIWALI	4 PL	8
Wed	8	5	10	7 DIWALI	5 PL	9
Thu	9	6	11	8	6 PL	10
Fri	10	7	12	9	7 PL	11
Sat	11	8	13	10	8 ESE	12
Sun	12	9	14	11	9	13
Mon	13	10	15	12	10 ESE	14
Tue	14	11	16	13	11 ESE	15
Wed	15 INDEPENDENCE DAY	12	17	14	12 ESE	16
Thu	16	13 GANESH CHATURTHI	18 MAHA NAVAMI	15 CT - 2	13 ESE	17
Fri	17	14	19 DUSSEHRA	16 CT - 2	14 ESE	18
Sat	18	15	20	17 CT - 2	15 ESE	19
Sun	19	16	21	18	16	20
Mon	20	17	22	19 CT - 2	17 ESE	21
Tue	21	18	23	20 CT - 2	18 GURU GHASIDAS JAYANTI	22
Wed	22 EID-UL-JUHA	19	24	21 EID-E-MILAD	19 ESE	23
Thu	23	20	25	22 FB-2	20 ESE	24
Fri	24	21 MUHARRAM	26	23 GURUNANAK JAYANTI	21 ESE	25
Sat	25	22	27	24 CTR-2	22 ESE	26
Sun	26 RAKHI	23	28	25	23	27
Mon	27	24	29	26 PTM-2	24 ESE	28
Tue	28	25 CT - 1	30	27	25 CHRISTMAS	29
Wed	29	26 CT - 1	31	28	26 ESE	30
Thu	30	27 CT - 1		29	27 ESE	31
Fri	31	28 CT - 1		30	28 PE	
Sat		29 CT - 1			29 PE	
Sun		30			30	
Mon					31 PE	
Tue						
Total Working Days	23	22	24	21		
Cumulative Working Days	23	45	69	90		

NOTE : Attendance will be displayed on the first working day of each month

I&O - INDUCTION & ORIENTATION
CT - CLASS TEST
ESE - END SEMESTER EXAM
PTM - PARENT-TEACHERS MEET

FB - FEEDBACK
CTR - CLASS TEST RESULTS
PE - PRACTICAL EXAM
PL - PREPARATION LEAVE

Dr. Arun Arora
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG C.G.

Academic Calender MBA 2ND & 4TH Semester, EVEN - 2019

DAYS	JAN 2019	FEB 2019	MAR 2019	APRIL 2019	MAY 2019	JUNE 2019
Mon				1		
Tue	1			2		
Wed	2			3	1	
Thu	3			4	2	
Fri	4	1	1 BITCON 2019	5	3	
Sat	5	2	2	6	4	1 ESE
Sun	6	3	3	7	5	2
Mon	7	4	4 Mahashivratri	8	6 PL	3 ESE
Tue	8	5	5	9 CLASS TEST-II	7	4 ESE
Wed	9	6	6	10 CLASS TEST-II	8	5 Id-Ul-Fitar
Thu	10	7	7	11 CLASS TEST-II	9	6 ESE
Fri	11	8	8 CLASS TEST- I	12 CLASS TEST-II	10	7
Sat	12	9	9 CLASS TEST- I	13 Ramnavmi	11	8 ESE
Sun	13	10	10	14	12	9
Mon	14 Makar Sankranti	11	11 CLASS TEST- I	15 CLASS TEST-II	13	10 ESE
Tue	15	12	12 CLASS TEST- I	16 CLASS TEST-II	14	11 ESE
Wed	16	13	13 CLASS TEST- I	17	15	12 ESE
Thu	17	14	14	18	16	13 ESE
Fri	18	15	15	19 Good Friday	17	14 ESE
Sat	19	16	16	20	18 Budh Purnima	15 ESE
Sun	20	17	17	21	19	16
Mon	21 Session Start	18	18	22	20	17 ESE PRACTICAL
Tue	22	19	19	23	21	18 ESE PRACTICAL
Wed	23	20	20 PARENT MEET	24	22	19 ESE PRACTICAL
Thu	24	21	21 Holi	25	23	20
Fri	25	22	22 Holi	26	24	21
Sat	26 Republic Day	23	23	27 CT-II RESULT	25	22
Sun	27	24	24	28	26	23
Mon	28	25	25 CT- I RESULT	29	27 ESE	24
Tue	29	26	26	30	28 ESE	25 ESE PRACTICAL
Wed	30	27 OJAS 2019	27		29 ESE	26 ESE PRACTICAL
Thu	31	28 OJAS 2019	28		30	27 ESE PRACTICAL
Fri			29		31 ESE	28
Sat			30			29
Sun			31			30
Mon						
Tue						
TWD	9	22	22	24		
CWD		31	53	77		

Note : Attendance will be displayed on the first working-day of each month

Principal
BIT Durg

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calender MBA- III Semester, ODD - 2018-19

DAYS	August 2018	September 2018	October 2018	November 2018	December 2018	January 2019
Mon			1 FB-1			
Tue			2 GANDHI JAYANTI			1 ESE
Wed	1		3 CTR-1			2 ESE
Thu	2		4	1		3 PE
Fri	3		5	2		4 PE
Sat	4	1	6	3	1 ESE	5 PE
Sun	5	2	7	4	2	6 PE
Mon	6 I&O	3 JANMASHATAMI	8	5 DIWALI	3 ESE	7 PE
Tue	7 I&O	4	9	6 DIWALI	4 ESE	8 PE
Wed	8 I&O	5	10	7 DIWALI	5 ESE	9
Thu	9	6	11	8	6 ESE	10
Fri	10	7	12	9	7 ESE	11
Sat	11	8	13	10	8 ESE	12
Sun	12	9	14	11	9	13
Mon	13	10	15	12	10 ESE	14
Tue	14	11	16	13	11 ESE	15
Wed	15 INDEPENDENCE DAY	12	17	14 PTM-2	12 ESE	16
Thu	16	13 GANESH CHATURTHI	18 MAHA NAVAMI	15 CT - 2	13 ESE	17
Fri	17	14	19 DUSSEHRA	16 CT - 2	14 ESE	18
Sat	18	15	20	17 CT - 2	15 ESE	19
Sun	19	16	21	18	16	20
Mon	20	17	22	19 CT - 2	17 ESE	21
Tue	21	18	23	20 CT - 2	18 GURU GHASIDAS JAYANTI	22
Wed	22 EID-UL-JUHA	19	24	21 EID-E-MILAD	19 ESE	23
Thu	23	20	25	22 FB-2 & CTR-2	20 ESE	24
Fri	24	21 MUHARRAM	26	23 GURUNANAK JAYANTI PL	21 ESE	25
Sat	25	22	27	24 PL	22 ESE	26
Sun	26 RAKHI	23	28	25 PL	23	27
Mon	27	24 PTM-1	29	26 PL	24 ESE	28
Tue	28	25 CT - 1	30	27 PL	25 CHRISTMAS	29
Wed	29	26 CT - 1	31	28 PL	26 ESE	30
Thu	30	27 CT - 1		29 PL	27 ESE	31
Fri	31	28 CT - 1		30 PL	28 ESE	
Sat		29 CT - 1			29 ESE	
Sun		30			30	
Mon					31 ESE	
Tue						
Total Working Days	21	22	24	15		
Cumulative Working Days	21	43	67	82		

NOTE : Attendance will be displayed on the first working day of each month

I&O - INDUCTION & ORIENTATION
CT - CLASS TEST
ESE - END SEMESTER EXAM
PTM - PARENT-TEACHERS MEET
FB - FEEDBACK
CTR - CLASS TEST RESULTS
PE - PRACTICAL EXAM
PL - PREPARATION LEAVE

Dr. Arun Arora
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic CalendarMBA IIISemester, ODD - 2017-18

DAYS	AUGUST 2017	SEPTEMBER 2017	OCTOBER 2017	NOVEMBER 2017	DECEMBER 2017
Mon					
Tue	1				
Wed	2			1	
Thu	3			2	
Fri	4	1		3	1ESE
Sat	5	2 ID-UL-JUHA		4 GURUNANAK JAYANTI	2 ID-E-MILAD
Sun	6	3	1	5	3
Mon	7 RAKSHA BANDHAN	4	2 GANDHI JAYANTI	6	4 ESE
Tue	8	5	3 Parent Teacher Meet 01(PTM01)	7	5 ESE
Wed	9	6	4	8	6 ESE
Thu	10 Session Starts	7	5	9	7 ESE
Fri	11	8	6	10	8 ESE
[illegible]	12	9	7	11	9 ESE
Sun	13	10	8	12	10
Mon	14	11	9	13	11 ESE
Tue	15 INDEPENDENCE DAY	12	10	14	12 ESE
Wed	16	13	11	15	13 ESE
Thu	17	14	12	16	14 ESE
Fri	18	15 Feedback 01	13	17 Feedback 02	15 ESE
Sat	19	16	14	18	16 ESE
Sun	20	17 Vishwakarma Pooja	15	19	17
Mon	21	18	16	20 CT02	18 GURU GHASIDAS JAYANTI
Tue	22	19	17	21CT02	19 ESE
Wed	23	20 CT01	18	22CT02	20 ESE
Thu	24	21 CT01	19 DEEPAWALI	23CT02	21 ESE
Fri	25 GANESH CHATURTHI	22 CT01	20 DEEPAWALI	24CT02	22 ESE
Sat	26	23 CT01	21 DEEPAWALI	25 PL / PTM02	23 ESE
[illegible]	27	24	22	26	24
[illegible]on	28	25 CT01	23	27PL / CT02 Results	25 CHRISTMAS
Tue	29	26	24	28 PL	26 ESE
Wed	30	27	25	29PL	27 ESE
Thu	31	28 CT01 Results	26	30PL	28 ESE
Fri		29 NAVAMI	27		29 ESE
Sat		30 DUSSHERA	28		30 ESE
Sun			29		31
Mon			30		ESE will be up-to 04/01/2018
Tue			31		Practical Exam will be from 05/01/2018 to 10/01/2018
Total Working days / per month	17	23	22	25	
Cumulative Working Days	17	40	62	87	

Note: Attendance will be displayed on the first working day of each month

Dr. Arun Arora
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG

Academic Calendar MBA ISemester, ODD - 2017-18

DAYS	AUGUST 2017	SEPTEMBER 2017	OCTOBER 2017	NOVEMBER 2017	DECEMBER 2017
Mon					
Tue	1				
Wed	2			1	
Thu	3 Session Starts			2	
Fri	4	1		3	1
Sat	5	2 ID-UL-JUHA		4 GURUNANAK JAYANTI	2 ID-E-MILAD / PL
Sun	6	3	1	5	3 PL
Mon	7 RAKSHA BANDHAN	4	2 GANDHI JAYANTI	6	4PL
Tue	8	5	3 Parent Teacher Meet 01(PTM01)	7	5PL
Wed	9	6	4	8	6PL
Thu	10	7	5	9	7PL
Fri	11	8	6	10	8 PL
[illegible]	12	9	7	11	9 ESE
Sun	13	10	8	12	10
Mon	14	11	9	13	11 ESE
Tue	15 INDEPENDENCE DAY	12	10	14	12 ESE
Wed	16	13	11	15	13 ESE
Thu	17	14	12	16	14 ESE
Fri	18	15 Feedback 01	13	17 Feedback 02	15 ESE
Sat	19	16	14	18	16 ESE
Sun	20	17 Vishwakarma Pooja	15	19	17
Mon	21	18	16	20 CT02	18 GURU GHASIDAS JAYANTI
Tue	22	19	17	21CT02	19 ESE
Wed	23	20 CT01	18	22CT02	20 ESE
Thu	24	21 CT01	19 DEEPAWALI	23CT02	21 ESE
Fri	25 GANESH CHATURTHI	22 CT01	20 DEEPAWALI	24CT02	22 ESE
Sat	26	23 CT01	21 DEEPAWALI	25PTM02	23 ESE
[illegible]	27	24	22	26	24
[illegible]on	28	25 CT01	23	27CT02 Results	25 CHRISTMAS
Tue	29	26	24	28	26 ESE
Wed	30	27	25	29	27 ESE
Thu	31	28 CT01 Results	26	30	28 ESE
Fri		29 NAVAMI	27		29 ESE
Sat		30 DUSSHERA	28		30 ESE
Sun			29		31
Mon			30		Practical Exam will be from 01/01/2018 to 05/01/2018
Tue			31		
Total Working days / per month	22	23	22	25	
Cumulative Working Days	22	45	67	92	

Note: Attendance will be displayed on the first working day of each month

Dr. Arun Arora
(Principal)

BHILAI INSTITUTE OF TECHNOLOGY, DURG C.G.

Academic Calender MBA 2ND & 4TH Semester, EVEN - 2018

DAYS	JAN 2018	FEB 2018	MAR 2018	APRIL 2018	MAY 2018	JUNE 2018
Mon	1					
Tue	2				1 Class Test-II	
Wed	3				2 Class Test-II	
Thu	4	1	1 Parent Meet		3 Class Test-II	
Fri	5	2	2 Holi		4 Class Test-II	1 ESE Theory
Sat	6	3	3 Holi		5 Class Test-II	2 ESE Theory
Sun	7	4	4	1	6	3
Mon	8 Session Start	5	5	2	7	4 ESE Theory
Tue	9	6	6	3	8	5 ESE Theory
Wed	10	7	7	4	9	6 ESE Theory
Thu	11	8 Feedback-I-2nd Sem	8	5	10 CT-II Result	7 ESE Theory
Fri	12	9 Feedback-I-4th Sem	9	6	11 PL	8 ESE Prac.
Sat	13	10	10	7	12 PL	9 ESE Prac.
Sun	14 Makar Sankranti	11	11	8	13	10
Mon	15	12 First Assignment	12	9	14 PL	11 ESE Prac.
Tue	16	13	13	10	15 PL	12 ESE Prac.
Wed	17	14 Mahashivratri	14	11	16 PL	13 ESE Prac.
Thu	18	15 BITCON	15	12	17 PL	14
Fri	19	16 OJAS 2018	16	13	18 PL	15
Sat	20	17 OJAS 2018	17	14 Ambedkar Jayenti	19 PL	16 Id-Ul_Fitar
Sun	21	18	18	15	20	17
Mon	22	19 Class Test-I	19	16	21 PL	18
Tue	23	20 Class Test-I	20	17 Second Assignment	22 ESE Theory	19
Wed	24	21 Class Test-I	21	18	23 ESE Theory	20
Thu	25	22 Class Test-I	22	19	24 ESE Theory	21
Fri	26 Republic Day	23 Class Test-I	23	20	25 ESE Theory	22
Sat	27	24	24	21	26 ESE Theory	23
Sun	28	25	25	22	27	24
Mon	29	26	26	23	28 ESE Theory	25
Tue	30	27	27	24	29 ESE Theory	26
Wed	31	28 CT-1 Result	28	25 Feedback-II - 2nd Sem	30 ESE Theory	27
Thu			29 Mahavir Jayenti	26 Feedback-II - 4th Sem	31 ESE Theory	28 Kabir Jayenti
Fri			30 Good Friday	27		29
Sat			31	28		30
Sun				29		
Mon				30 Budh Purnima		
Tue						
TWD	20	20	23	23	9	
CWD		40	63	86	95	

Note : Attendance will be displayed on the first working-day of each month

Principal
BIT Durg

2.3.4 Preparation and adherence to Academic Calendar and Teaching plans by the institution Response:

Academic Calendar

The Dean for Academic Affairs in consultation with the Director, Principal, Vice Principal, Coordinator of Internal Quality Assurance Cell, HOD'S and other officials, prepares the Academic Calendar for the effective functioning of the Institution. The academic calendar of the university is taken for the reference.

The Academic Calendar is prepared for OOD and EVEN session separately. The Academic Calendar ensures well-functioning of the Institution. The Calendar includes the details of all the working days with day order, holidays, dates of the Continuous Internal Assessment Tests, End Semester Examinations, list of activities of academic year and the dates of national as well as religious importance. The calendar of common programmes is circulated to all students at the beginning of the academic year. The same is also uploaded on the website.

The institution carries out the activities as per the calendar. The Departments and other service units prepare their activities and programmecalendar resonating with the calendar of common programmes. It helps micro level planningby scheduling most of the activities in advance and maximise the use of infrastructure and ICTfacilities.The handbook provides all needed information such as academic programmes,curriculum structure, details of the courses, rules, regulations, facilities, scholarships,endowments, list of the staff, committees, and so on. This almanac gears up for the overallfunctioning of the Institution.

Teaching plans are prepared by the concern teachers for respective subjects and submitted to the HOD'S. Teaching plans are supported by assignment sheets and tutorial sheets.

Documents:

1. Teaching Plan:

ASPIRE TO EXCEL	**LESSON PLAN**	**LP – 328551 (28)**
	Sub Code & Name : 328551 (28) Linear integrated circuits and applications	**Date: 04.07.16**
		Page 01 of 09
	Unit:1 ***Branch: E T & T*** ***Semester: V***	

Name of Faculty: Mrs. K.Uma
Faculty Email ID:umakarnam@rediffmail.com

Unit syllabus: **Fundamentals of differential amplifiers and operational amplifiers:** Current mirror, BJT differential amplifier analysis using r-parameters. Introduction to operational amplifier : op-amp Symbol and terminal characteristics, Block schematic of op-amp, Ideal op-amp characteristics, Open loop configuration of op-amp, Closed loop configuration of op-amp: Voltage series feedback amplifier, Voltage shunt feedback amplifier, Differential amplifier. The practical opamp: Input offset voltage, Input bias current, Input offset current, Total output offset voltage, Thermal drift. Frequency response of an op-amp: Frequency response, Compensating networks, Slew rate.

Objective: In this Unit students will acquire knowledge of working of differential amplifier the basic block of op-amp. Op-amp characteristics both ideal and practical and also different configurations will be learnt.

S. No	Day order	Topics to be covered	Date planned	Date covered
		Fundamentals of differential amplifiers and operational amplifiers :		
1.	1	BJT differential amplifier	**4-7-16**	4-7-16
2.	2	Differential amplifiers working.	5-7-16	11-7-16
3.	3	Types of differential amplifiers and working analysis of dual input diff. amp.	7-7-16	12-7-16
4.	4	Single input differential amplifier analysis and characteristics.	8-7-16	13-7-16
5.	5	Differential amplifier with active load and discuss about current mirror	9-7-16	14-7-16
6.	6	Block diagram of op-amp and ideal characteristics.	11-7-16	15-7-16
7.	7	Electrical parameters of op-amp and their ranges.	12-7-16	18-7-16
8.	8	Open loop and closed loop configuration of op-amps	13-7-16	19-7-16
9.	9	Voltage series feedback amplifier	14-7-16	20-7-16
10.	10	Voltage shunt feedback amplifier	15-7-16	22-7-16
11.	11	Differential amplifier.	18-7-16	25-7-16
12.	12	The practical opamp : Input offset voltage	19-7-16	26-7-16
13.	13	Input bias current	20-7-16	27-7-16
14.	14	Input offset current, Total output offset voltage	21-7-16	28-7-16
15.	15	Thermal drift and related numerical.	22-7-16	29-7-16
16.	16	Frequency response of an op-amp: Frequency response	25-7-16	1-8-16
17.	17	Compensating networks	26-7-16	3-8-16
18.	18	Slew rate and related numerical.	27-7-16	4-8-16

	LESSON PLAN	
	Sub Code & Name : 328551 (28) Linear integrated circuits and applications	**LP – 328551 (28)** **Rev. No:02** **Date: 04.07.16**
	Unit:II ***Branch: E T & T*** ***Semester: V***	**Page 03 of 09**

Unit syllabus:

Applications of OPAMP: Operational amplifier applications: Basic op-amp circuits: Summing, Scaling and Averaging amplifiers. Comparator: Inverting and Non-inverting comparator, Schmitt trigger, Zero crossing detector, Peak detector, Level detector, Window detector, Rectifier: Precision half wave rectifier, Precision full wave rectifier, Current to voltage and Voltage to current converter, Bridge amplifier; Instrumentation amplifier. Differentiator, Integrator, Logarithmic amplifier, Norton amplifier.

Objective: The students must acquire a thorough knowledge of Various applications of operational amplifiers.

S. No	Day order	Topics to be covered	Date planned	Date covered
		Operational amplifier applications:		
15	15	Summing, Scaling and Averaging amplifiers	28-7-16	8-8-16
16	16	Comparator: Inverting and Non-inverting comparator	29-7-16	9-8-16
17	17	Zero crossing detector and Schmitt trigger.	1-8-16	10-8-16
18	18	Peak detector, Level detector	2-8-16	11-8-16
19	19	Window detector, numerical	3-8-16	23-8-16
20	20	Rectifier: Precision half wave rectifier	4-8-16	24-8-16
21	21	Precision full wave rectifier	8-8-16	26-8-16
22	22	Current to voltage and Voltage to current converter	9-8-16	6-9-16
23	23	bridge amplifier, Instrumentation amplifier	10-8-16	7-9-16
24	24	Differentiator, working analysis and design problems.	11-8-16	8-9-16
25	25	Integrator, working analysis and design problems.	12-8-16	9-9-16
26	26	Logarithmic Amplifier	16-8-16	15-9-16
27	27	Norton Amplifier.	17-8-16	16-9-16
28	28	Doubts in unit and discussion of previous year question papers.	19-8-16	16-9-16

	LESSON PLAN	
	Sub Code & Name : 328551 (28) Linear integrated circuits and applications	**LP – 328551 (28)** **Date: 04.07.16** **Page 04 of 09**
	Unit:III ***Branch: E T & T*** ***Semester: V***	

Unit syllabus:

Analog to digital and digital to analog converters: Sample and hold circuits and sample and hold IC (LF 398), Types of D/A converter : The binary weighted resistor network, The R-2R ladder network, The inverted ladder, D/A specification. A/D converter : Parallel-comparator type, Dual slope, Successive approximation, Voltage to time and Voltage to frequency converters, A/D specification.

Objective: The students must acquire a thorough knowledge of Analog to Digital and Digital to analog Circuits.

S. No	Day order	Topics to be covered	Date planned	Date covered
		Analog to digital and digital to analog converters:		
29	39	Sample and hold circuits and sample and hold IC (LF 398).	30-8-16	27-9-16
30	40	The binary weighted resistor network.	31-8-16	28-9-16
31	41	The R-2R ladder network.	1-9-16	29-9-16
32	42	The inverted ladder, related numerical.	2-9-16	29-9-16
33	43	D/A specification	6-9-16	30-9-16
34	44	Parallel-comparator type	7-9-16	30-9-16
35	45	Dual slope A/D converter	8-9-16	3-10-16
36	46	Successive approximation A/D converter.	9-9-16	4-10-16
37	47	Voltage to time converters	13-9-16	5-10-16
38	48	Voltage to frequency converters	14-9-16	6-10-16
39	49	A/D specification	15-9-16	7-10-16

	LESSON PLAN	
	Sub Code & Name : 328551 (28) Linear integrated circuits and applications	LP – 328551 (28) Rev. No:04 Date: 04.07.16
	Unit:IV *Branch: E T & T* *Semester: V*	Page 06 of 09

Unit syllabus: **Voltage Regulators:** Voltage regulator characteristics, Regulator performance parameters, Types of voltage regulator: Series and Shunt regulator using op-amp. Safe operating Area, Protection circuit: Short circuit protection, Current limiting circuit, Foldback current limiting, Three terminal IC regulator: Three terminal IC regulator(LM 317, LM 337, 78XX, 79XX) [Description, Schematic diagram and Pin diagram], General purpose IC regulator (723): Important features and Internal structure..

Objective: The students must learn voltage regulator circuits and their performance parameters and also some specific Voltage regulator ICs.

LECT. No	Day order	Topics to be covered	Date Planned	Date covered
		Voltage Regulators:		
40	29	Voltage regulator characteristics, Regulator performance parameters	16-9-16	17-9-16
41	30	Types of voltage regulator: Series and Shunt regulator using op-amp.	17-9-16	17-9-16
42	31	Safe operating Area, Protection circuit: Short circuit protection	19-9-16	19-9-16
43	32	Current limiting circuit, Foldback current limiting	20-9-16	20-9-16
44	33	IC regulator(78XX, 79XX) [Description, Schematic diagram and Pin diagram]	21-9-16	21-9-16
45	34	IC regulator(LM 317, LM 33) [Description, Schematic diagram and Pin diagram]	22-9-16	22-9-16
46	35	General purpose IC regulator (723): Important features and Internal structure.	23-9-16	22-9-16
47	36	General purpose IC regulator (723): Important features and Internal structure.	26-9-16	23-9-16
48	37	Related Numericals.	27-9-16	23-9-16
49	38	Disscussions of questions from previous years papers	28-9-16	23-9-16

	LESSON PLAN	
	Sub Code & Name : 328551 (28) Linear integrated circuits and applications	**LP – 328551 (28)** **Rev. No:05** **Date: 04.07.16**
	Unit:V ***Branch: E T & T*** ***Semester: V***	**Page 07 of 09**

Unit syllabus:

PLL and multiplier: Phase Locked Loops: Introduction, Basic principle. Phase detector: Analog and Digital**,** Voltage controlled oscillator, PLL IC 565: Functional diagram and Principle of operation, Derivation of lock-in and capture Range. Application of PLL (AM detection, FM detection, FSK demodulation and frequency synthesizer).
Analog Multipliers and Dividers: Analog Multiplier: Introduction, Characteristics. Multiplication techniques: Logarithmic summing, PWM, Variable transconductance. Monolithic multiplier circuit realization: Logarithmic - Exponential multiplier circuit (RC-4200). Multiplier application: Divider circuit, Square rooting circuit, RMS detector.

Objective: Students must be able to understand the fundamentals of PLL and also be able to design multiplier circuits and applications.

S.No.	Day order	Topics to be covered	Date Planned	Date covered
		PLL and multiplier:		
50	50	Phase Locked Loops: Introduction, Basic principle	29-9-16	7-10-16
51	51	Phase detector: Analog and Digital	30-9-16	8-10-16
52	52	Voltage controlled oscillator	3-10-16	8-10-16
53	53	IC 565: Functional diagram and Principle of operation	4-10-16	13-10-16
54	54	Derivation of lock-in and capture Range	5-10-16	13-10-16
55	55	Application of PLL (AM detection, FM detection)	6-10-16	14-10-16
56	56	FSK demodulation and frequency synthesizer	7-10-16	14-10-16
57	57	Analog Multiplier: Introduction, Characteristics	13-10-16	24-10-16
58	58	Multiplication techniques: Logarithmic summing, PWM	14-10-16	24-10-16
59	59	Variable transconductance	17-10-16	24-10-16
60	60	Monolithic multiplier circuit realization: Logarithmic - Exponential multiplier circuit (RC-4200)	18-10-16	25-10-16
61	61	Multiplier application: Divider circuit, Square rooting circuit, RMS detector.	19-10-16	27-10-16
62	62	Numericals based	20-10-16	27-10-16

	LESSON PLAN	
	Sub Code & Name : 328551 (28) Linear integrated circuits and applications *Branch: E T & T* *Semester: V*	**LP – 328551 (28)** **Date: 04.07.16** **Page 09 of 09**

Course Delivery Plan:

Week	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII	XIII	XIV
Unit														

Name of Text Book:

1. Integrated Circuits by K. R. Botkar, 9th Ed., Khanna Publications (Unit- I,II,III,IV,V)
2. Operational Amplifiers by R. Gayekwad, 4th Ed., Pearson Education(Unit-I, II.IV)
3. Linear Integrated Circuits by D.Roy Choudhary and Shail B Jain,3rd Ed., New Age International (Unit-IV,V)
4. Digital integrated Electronics by Herbert Taub and Donald Schilling, McGraw Hill(Unit- III)

Name of Reference Books:

1. Integrated Electronics by Millman& Halkias,6th Ed., TMH Publishing Co.
2. Operational Amplifiers and Linear Integrated Circuits, Lal Kishore,2nd Ed., PHI
3. Operational Amplifiers and Linear Integrated Circuits, Coughlin and Driscoll, 6th Ed., PHI

	Prepared by	Approved by
Signature		
Name	**Mrs. K.Uma**	**Dr.(Mrs.) Manisha Sharma**
Designation	**Asso. Prof. of Electronics & Telecommunication**	**HOD- Deptt of Electronics & Telecommunication**
Date	**4.07.16**	

ASPIRE TO EXCEL	**LESSON PLAN** ***Sub Code & Name : 328453(28) Analog Electronics*** ***Unit:1*** ***Branch: E T & T*** ***Semester: IV***	**LP – 328551 (28)** **Date: 01.01.16** **Page 01 of 09**

Name of Faculty: Mrs. K.Uma
Faculty Email ID:umakarnam@rediffmail.com

UNIT- I BJT AT LOW FREQUENCY: Transistor as a two port device and its Hybrid Model: Models for CB, CE, CC configurations and their Interrelationship, Analysis and Comparison of the three Configurations. Classification of Amplifiers, Amplitude and Frequency, Linear analysis of Transistor Circuits. Miller's Theorem and its dual. Cascading transistor Amplifiers. Simplified Models and Calculation of CE and CC Amplifiers. The Common Emitter Amplifier with an Emitter Resistance. Cascode Amplifiers. High Input resistance Transistor Circuits.

Objective: In this Unit students will acquire knowledge of Transistor as a two port device and analysis of transistor as an amplifier using h-parameter model.

S.No	Day order	Topics to be covered	Date planned	Date covered
		BJT AT LOW FREQUENCY:		
1.	1	Transistor as a two port device	4-1-16	4-1-16
2.	2	Hybrid Model	5-1-16	5-1-16
3.	3	Models for CB, CE, CC configurations	6-1-16	7-1-16
4.	4	Analysis of CB, CE, CC configurations.	7-1-16	8-1-16
5.	5	Comparison of the three Configurations	8-1-16	9-1-16
6.	6	Interrelationship of three configurations.	9-1-16	11-1-16
7.	7	Classification of Amplifiers, Amplitude and Frequency.	11-1-16	14-1-16
8.	8	Linear analysis of Transistor Circuits	12-1-16	15-1-16
9.	9	Miller's Theorem and its dual.	13-1-16	18-1-16
10.	10	Numerical based	15-1-16	19-1-16
11.	11	Cascading transistor Amplifiers	16-1-16	21-1-16
12.	12	Numericals related	18-1-16	21-1-16
13.	13	Simplified Models and Calculation of CE Amplifiers	19-1-16	22-1-16
14.	14	Simplified Models and Calculation of CC Amplifiers	20-1-16	22-1-16
15.	15	The Common Emitter Amplifier with an Emitter Resistance	21-1-16	23-1-16
16.	16	Cascode Amplifiers	22-1-16	25-1-16
17.	17	High Input resistance Transistor Circuits. Darlington circuits and numerical related.	23-1-16	27-1-16
18.	18	Bootstrap circuits	25-1-16	28-1-16

	LESSON PLAN	
	Sub Code & Name : 328453(28) Analog Electronics	**LP – 328453 (28)** **Rev. No:02**
	Unit:II ***Branch: E T & T*** ***Semester: IV***	**Date: 01.01.16** **Page 03 of 09**

UNIT-II BJT AT HIGH FREQUENCY: CE hybrid-□model, Hybrid –π Conductances and Capacitances. Validity and parameter Variation, CE Short Circuit Current Gain, Current Gain with Resistive load. Frequency response of a single stage CE Amplifier, Gain-Bandwidth product, CC stage High frequencies.

Objective:

The students must acquire a thorough knowledge of operation of BJT at High frequencies.

S.No	Day order	Topics to be covered	Date planned	Date covered
		BJT AT HIGH FREQUENCY		
19	19	CE hybrid-π model	27-1-16	28-1-16
20.	20	Hybrid –π Conductances and Capacitances	28-1-16	1-2-16
21	21	Validity and parameter Variation	29-1-16	2-2-16
22	22	CE Short Circuit Current Gain	30-1-16	4-2-16
23	23	CE Short Circuit Current Gain with Resistive load.	1-2-16	5-2-16
24	24	Numerical related	2-2-16	6-2-16
25	25	Frequency response of a single stage CE Amplifier	3-2-16	8-2-16
26	26	Frequency response of a single stage CE Amplifier with load	4-2-16	9-2-16
27	27	Gain-Bandwidth product	5-2-16	10-2-16
28	28	CC stage High frequencies	6-2-16	11-2-16
29	29	Numericals Related.	8-2-16	11-2-16

	LESSON PLAN	
ASPIRE TO EXCEL	*Sub Code & Name : 328453(28) Analog Electronics* ***Unit:III*** ***Branch: E T & T*** ***Semester: IV***	**LP – 328453 (28)** **Date: 01.01.16** **Page 04 of 09**

UNIT- III MULTISTAGE AMPLIFIERS: Introduction, Distortion in Ampli1iers, Frequency Response, Step Response of an amplifier, Band Pass of Cascaded Stages. Coupling Types: Direct, RC and Transformer. RC Coupled Amplifier, Low Frequency response of an RC-coupled Stage, Effect of an Emitter bypass capacitor, High Frequency response of two cascaded CE Transistor stages.
Power Amplifiers: Class A Large signal amplifiers and Class B Amplifier: Conversion Efficiency and Distortion. Class AB Operation, Push pull amplifiers.

Objective:

The students must acquire a thorough knowledge of Multistage amplifiers, types of coupling to be used for different applications and also different types of large signal amplifiers.

S.No	Day order	Topics to be covered	Date planned	Date covered
		MULTISTAGE AMPLIFIERS:		
30	30	Introduction	9-2-16	12-2-16
31	31	Distortion in Ampli1iers	10-2-16	12-2-16
32	32	Frequency Response	11-2-16	13-2-16
33	33	Step Response of an amplifier	12-2-16	10-2-16
34	34	BandPass of Cascaded Stages..	15-2-16	22-2-16
35	35	Numerical related	16-2-16	23-2-16
35	35	Coupling Types: Direct, RC and Transformer.	17-2-16	25-2-16
36	36	RC Coupled Amplifier.	18-2-16	1-3-16
37	37	Low Frequency response of an RC-coupled Stage.	19-2-16	3-3-16
38	38	Effect of an Emitter bypass capacitor.	22-2-16	4-3-16
39	39	High Frequency response of two cascaded CE Transistor stages.	23-2-16	5-3-16
40	40	Numerical Related.	24-2-16	9-3-16
41	41	Doubts in unit and discussion of previous year question papers.	25-2-16	9-3-16
42	42	Class A Large signal amplifiers	26-2-16	10-3-16
43	43	Conversion Efficiency and Distortion of Class A amplifier.	29-2-16	10-3-16
44	44	Class B Amplifier, Conversion Efficiency and Distortion	1-3-16	12-3-16
45	45	Class AB Operation	2-3-16	12-3-16
46	46	Push pull amplifiers.	3-3-16	14-3-16

	LESSON PLAN	
	Sub Code & Name : 328453(28) Analog Electronics	LP – 328453 (28) Rev. No:04
	Unit:IV *Branch: E T & T* *Semester: IV*	Date: 01.01.16 Page 06 of 09

UNIT-IV FEEDBACK AMPLIFIERS: Classification, Feedback concept, Transfer gain with Feedback, Characteristics of Negative Feedback Amplifiers, Analysis of Input and output Resistance. Topologies: Method of Analysis of Feedback amplifiers, Voltage series Feedback, Voltage series Feedback pair, Current series, Current shunt and Voltage shunt feedback. Concept of positive Feedback.

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Objective: The students must learn Feedback concepts and different topologies of feedback amplifiers.

LECT. No	Day order	Topics to be covered	Date Planned	Date covered
		FEEDBACK AMPLIFIERS :		
47	47	Classification	4-3-16	15-3-16
48	48	Feedback concept	8-3-16	15-3-16
49	49	Transfer gain with Feedback	9-3-16	17-3-16
50	50	Characteristics of Negative Feedback Amplifiers	10-3-16	18-3-16
51	51	Analysis of Input and output Resistance	11-3-16	19-3-16
52	52	Topologies: Method of Analysis of Feedback amplifiers	14-3-16	21-3-16
53	53	Voltage series Feedback amplifier.	15-3-16	21-3-16
54	54	Current series feedback amplifier	16-3-16	28-3-16
55	55	Current shunt feedback amplifier	17-3-16	29-3-16
56	56	Voltage shunt feedback amplifier	18-3-16	31-3-16
57	57	Concept of positive Feedback.	21-3-16	31-3-16
58	58	Related Numerical	22-3-16	1-4-16

	LESSON PLAN	
	Sub Code & Name : 328453(28) Analog Electronics	**LP – 328453(28)** **Rev. No:05** **Date: 01.01.16**
	Unit:V ***Branch: E T & T*** ***Semester: IV***	**Page 07 of 09**

UNIT-V OSCILLATOR (BJT): Barkhausen criterion for oscillation, Mechanism for start of oscillation and Stabilization of amplitude, Analysis of RC and LC oscillators. Sinusoidal oscillator: Phase shift oscillators, Wien Bridge oscillator, Resonant circuit oscillators, Colpitts and Hartley oscillator. Amplitude Frequency and Phase stability analysis of all Oscillators, General form of Oscillator Configuration. Crystal oscillator.

Objective: Students must be able to understand Oscillator circuits and also study the design and applications of different oscillators.

S.No.	Day order	Topics to be covered	Date Planned	Date covered
		OSCILLATOR (BJT):		
59	59	Barkhausen criterion for oscillation	28-3-16	2-4-16
60	60	Mechanism for start of oscillation and Stabilization of amplitude	29-3-16	4-4-16
61	61	Analysis of RC and LC oscillators	30-3-16	5-4-16
62	62	Phase shift oscillators	31-3-16	7-4-16
63	63	Wien Bridge oscillator	1-4-16	8-4-16
64	64	Resonant circuit oscillators, Colpitts and Hartley oscillator	4-4-16	9-4-16
65	65	General form of Oscillator Configuration. Crystal oscillator.	5-4-16	11-4-16
66	66	Numericals related to the unit.	6-4-16	12-4-16
67	67	Discussions of questions from previous years papers.	7-4-16	13-4-16

Name of Faculty: Dr. Amar Kumar

	LESSON PLAN	
	Sub Code & Name : 328453(28) Analog Electronics	**LP – 328453(28)** **Date: 01.01.16**
	Branch: E T & T ***Semester: IV***	**Page 09 of 09**

Course Delivery Plan:

Week	**I**	**II**	**III**	**IV**	**V**	**VI**	**VII**	**VIII**	**IX**	**X**	**XI**	**XII**	**XIII**	**XIV**
Unit														

Text Books:

1. Integrated Electronics – Millman & Halkias, Tata McGraw Hill. (Unit I to V)
2. Microelectronics – Millman and Grabel, Tata McGraw Hill.
3. Electronic Devices & Circuits – Donald A Neaman, Tata McGraw Hill.

Reference Books:

1. Electronic devices and circuits- A.K. Maini & Varsha Agrawal, 1st Edition ,Wiley Publication.
2. Electronic Devices & Circuits – David A. Bell, PHI.
3. Microelectronic Circuits- Sedra and Smith, 5th Edition, Oxford University Press.

	Prepared by	**Approved by**
Signature		
Name	**Mrs. K.Uma**	**Dr.(Mrs.) Manisha Sharma**
Designation	**Asso. Prof. of Electronics & Telecommunication**	**HOD- Deptt of Electronics & Telecommunication**
Date	**01.01.16**	

Name of Faculty: Dr. Amar Kumar

	LESSON PLAN	
ASPIRE TO EXCEL	**Sub Code & Name: 328452 (28) Analog Communication** ***Unit: I*** ***Branch: E T & T*** ***Semester: IV***	LP – B028411(028) Date: 08/03/2021 Page 01 of 06

Unit syllabus:

Unit – I: Introduction to Communication System

Introduction: Overview of Communication system, Communication channels , Need for modulation, Baseband and Passband signals .Classification of signals and study of Fourier transforms for standard signals, definition of signal bandwidth, Distortion less transmission, Parseval's Theorem. Introduction to Convolution and correlation of signals, comparison between correlation and convolution. Frequency division multiplexing.

Objective:

After going through this unit students will be able to:

- Have a clear idea of the meaning of modulation and the need of modulation.
- Give the time domain representation spectrum and the methods of generation and detection of amplitude modulated signals.

Sr. No.	Topics to be covered	Date Planned	Date Covered
	Introduction to Communication System		
01	Introduction: Overview of Communication system, Communication channels, Need for modulation.	08/03/21	08/03/21
02	Baseband and Passband signals .Classification of signals and study of Fourier transforms for standard signals.	09/03/21	08/03/21
03	Definition of signal bandwidth, Distortion less transmission, Parseval's Theorem.	10/03/21	09/03/21
04	Introduction to Convolution and correlation of signals,	12/03/21	15/03/21
05	Comparison between correlation and convolution	15/03/21	16/03/21
06	Frequency division multiplexing.	16/03/21	18/03/21
07	Numericals	17/03/21	22/03/21

Name of Faculty: Dr. Amar Kumar

	LESSON PLAN	LP – B028411(028)
ASPIRE TO EXCEL	**Sub Code & Name: 328452 (28) Analog Communication**	Date: 08/03/2021
	Unit: II ***Branch: E T & T*** ***Semester: IV***	Page 02 of 06

Unit syllabus:

Unit – II: Amplitude Modulation

Amplitude Modulation: full carrier system and Suppressed carrier system . Double side band with full Carrier, Generation and Detection of Double side band without Carrier (DSB-SC), SSB-SC, VSB-SC, Single Side Band Modulation, Phasor representation, Bandwidth, Modulation Index Superposition Theorem of Spectra. Power Content in AM signal. Generation of AM using LTI circuits and Non-linear circuits.Demodulation of AM waves: Square law detectors and Envelope detectors

Objective:

After going through this unit students will be able to:

- Understand the concept of amplitude modulation.
- Derive expression for amplitude modulation.
- Recognize the key specification for an AM generations, draw its block diagram, and explain its working.

Sr. No.	Topics to be covered	Date Planned	Date Covered
	Amplitude Modulation		
08	Amplitude Modulation: full carrier system and Suppressed carrier system.	20/03/21	24/03/21
09	Double side band with full Carrier, Generation and Detection of Double side band without Carrier (DSB-SC)	22/03/21	25/03/21
10	SSB-SC	23/03/21	01/04/21
11	VSB-SC, Single Side Band Modulation, Phasor representation	24/03/21	05/04/21
12	Bandwidth, Modulation Index Superposition Theorem of Spectra. Power Content in AM signal.	03/04/21	06/04/21
13	Generation of AM using LTI circuits and Non-linear circuits	05/04/21	07/04/21
14	Demodulation of AM waves: Square law detectors	06/04/21	08/04/21
15	Envelope detectors	07/04/21	12/04/21
16	Numerical	10/04/21	13/04/21
17	Numerical	12/04/21	14/04/21

Name of Faculty: Dr. Amar Kumar

	LESSON PLAN	LP – B028411(028)
ASPIRE TO EXCEL	**Sub Code & Name: 328452 (28) Analog Communication**	Date: 08/03/2021
	Unit: III ***Branch: E T & T*** ***Semester: IV***	Page 03 of 06

Unit syllabus:

Unit – III: Angle modulation

Angle modulation, Phase & frequency modulation, Relationship between phase and frequency modulation, Phase and frequency deviation, Spectrum of an FM signal, Bandwidth and power of a sinusoidal modulated FM signal, Types of FM: Narrowband FM and Wideband FM. Phasor diagram for FM signals. FM generation: Parameter-variation method, an indirect method of frequency modulation (Armstrong system), Frequency multiplication, and Frequency multiplication applied to FM signals, FM demodulators: Slope detectors and Phase difference discriminators. Comparison of AM and FM.

Objective:

After going through this unit students will be able to:

- Define angle modulation and describe the types of FM.
- Explain FM generation and demodulations.

Sr. No.	Topics to be covered	Date Planned	Date Covered
	Angle modulation		
18	Angle modulation, Phase & frequency modulation, Relationship between phase and frequency modulation	15/04/21	15/04/21
19	Phase and frequency deviation, Spectrum of an FM signal, Bandwidth and power of a sinusoidal modulated FM signal,	17/04/21	19/04/21
20	Types of FM: Narrowband FM and Wideband FM.	19/04/21	19/04/21
21	Phasor diagram for FM signals.	20/04/21	20/04/21
22	FM generation: Parameter-variation method, an indirect method of frequency modulation (Armstrong system),	21/04/21	20/04/21
23	Frequency multiplication, and Frequency multiplication applied to FM signals	23/04/21	21/04/21
24	FM demodulators : Slope detectors and Phase difference discriminators.	24/04/21	26/04/21
25	Comparison of AM and FM.	26/04/21	26/04/21
26	Numericals	27/04/21	27/04/21
27	Numericals	28/04/21	29/04/21

Name of Faculty: Dr. Amar Kumar

	LESSON PLAN	
ASPIRE TO EXCEL	**Sub Code & Name: 328452 (28) Analog Communication** ***Unit: IV*** ***Branch: E T & T*** ***Semester: IV***	LP – B028411(028) Date: 08/03/2021 Page 04 of 06

Unit syllabus:

Unit – IV: **Transmitters and Receivers**

AM Transmitters: Generation of AM, low level and high level modulation, comparison of levels, AM transmitter block diagram, collector class C modulator, Base Modulator, DSB -SC modulator. FM transmitter: Direct Method , Armstrong Indirect Method Radio Receivers and Demodulators
:Introduction, Performances characteristic of receivers: Sensitivity, Selectivity, Fidelity, Image frequency and IFRR, Tracking and Double spotting, TRF, Super heterodyne receivers AGC. PLL for FM demodulation.

Objective:

After going through this unit students will be able to:

- Identify different AM and FM receivers.
- Explain the advantage of using superheterodyne receiver over TRF receiver.
- Recognize the factor effecting signal to noise ratio in DSB-SC, SSB-SC, square law and envelope demodulator.

Sr. No.	Topics to be covered	Date Planned	Date Covered
	Transmitters and Receivers		
28	AM Transmitters: Generation of AM, low level and high level modulation, comparison of levels, AM transmitter block diagram,	29/04/21	03/05/21
29	Collector class C modulator, Base Modulator, DSB -SC modulator.	30/04/21	04/05/21
30	FM transmitter: Direct Method , Armstrong Indirect Method Radio Receivers and	01/05/21	06/05/21
31	Demodulators : Introduction, Performances characteristic of receivers: Sensitivity, Selectivity, Fidelity, Image frequency and IFRR, Tracking and Double spotting,	03/05/21	17/05/21
32	TRF, Super heterodyne receivers AGC.	04/05/21	18/05/21
33	PLL for FM demodulation.	17/05/21	18/05/21
34	Numericals	18/05/21	20/05/21

Name of Faculty: Dr. Amar Kumar

	LESSON PLAN	
ASPIRE TO EXCEL	**Sub Code & Name: 328452 (28) Analog Communication** ***Unit: V*** ***Branch: E T & T*** ***Semester: IV***	LP – B028411(028) Date: 08/03/2021 Page 05 of 06

Unit syllabus:

Unit – V: Noises in Analog Communication:

Noise Introduction, Sources of Noise, Classification of noise, Noise calculations (thermal noise), SNR, Noise figure for cascaded amplifiers, Noise Factor, Effective input Noise Temperature. Noise calculation (SNR, FOM) of Various AM system: DSB-SC, SSB-SC, AM-FC system (Envelope detector) Threshold Effect in Envelope detector. Noise in angle modulated system: Capture effect, Threshold effect and its improvement in Discriminators

Objective:

After going through this unit students will be able to:

- Analyze the noise performance of FM.
- Understand how and why it is possible to improve the destination signal to noise ratio.

Sr. No.	Topics to be covered	Date Planned	Date Covered
	Noises in Analog Communication:		
35	Noise Introduction, Sources of Noise,	20/05/21	24/05/21
36	Classification of noise, Noise calculations (thermal noise), SNR, Noise figure for cascaded amplifiers, effect and its improvement in Discriminator	24/05/21	24/05/21
37	Noise Factor, Effective input Noise Temperature. Noise calculation (SNR, FOM) of Various AM system: DSB-SC, SSB-SC	25/05/21	25/05/21
38	AM-FC system (Envelope detector) Threshold Effect in Envelope detector.	27/05/21	27/05/21
39	Noise in angle modulated system: Capture effect Threshold effect and its improvement in Discriminators	28/05/21	29/05/21
40	Numericals	29/05/21	29/05/21

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	LP – B028411(028)
ASPIRE TO EXCEL	**Sub Code & Name: 328452 (28) Analog Communication**	Date: 08/03/2021
	Unit: I to V Branch: E T & T Semester: IV	Page 06 of 06

Course Delivery Plan:

Week	1	2	3	4	5	6	7	8	9	10	11	12	13		
Unit	I	I	II	II	II	III	III	III	IV	IV	IV-V	V	V		

References:

Name of Text Books:

1. Principles of Communication Systems, Taub and Schilling, 2nd Edition, Tata McGraw Hill.
2. Electronic Communication Systems, George F Kennedy, Tata McGraw Hill.
3. Communication Systems, Simon Haykins, Wiley India.

Name of Reference Books:

4. Communication Systems Engineering, Proakis, 2nd Edition, Pearson Education.
5. Modern Digital and Analog Communication, B.P. Lathi, Oxford University Press.

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	Prepared by	Approved by
Signature		
Name	**Dr. Amar Kumar Dey**	**Dr. Naveen Kr. Dewangan**
Designation	**Assistant Professor** **Dept. of Electronics & telecommunication**	**Professor & Head** **Dept. of Electronics & telecommunication**
Date	**01/03/2021**	29/05/21

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	
	Sub Code & Name ***: B028313(028) Digital System Design***	**LP –** ***B028313(028)*** **Rev. No:01 Date: 01.07.20**
	Unit:I ***Branch: E T & T*** ***Semester: III***	**Page 01 of 06**

Unit I: **Boolean Algebra & Minimization Techniques:** Logic Simplification and Combinational Logic Design, Boolean Algebra: Logic Operations; Axioms and Laws of Boolean Algebra: Complementation Laws, AND Laws, OR Laws, Commutative Laws, Associative Laws, Distributive Laws, Absorption Laws, Transposition Theorem, De Morgan's Theorem; Duality; Reducing Boolean Expressions; Functionally Complete Sets of Operations; Boolean Functions and their Representation. Minimization Techniques: Expansion of a Boolean expression to SOP form; Expansion of a Boolean expression to POS form; Karnaugh maps up to 4 variables, Mapping and minimization of SOP and POS expressions; Concept of Don't Care Terms; Quine – McClusky Method (Up to 5 variable); Synthesis using AND-OR, NAND-NOR and XOR forms; Design Examples; Binary codes and code conversion (BCD, Excess-3, Gray code)

Objective: In this Unit students will acquire knowledge of various binary codes and their conversions. Errors in codes and the corrections that can be done. Boolean expressions, various laws, forms and their realization.

LECT. No	Date planned	Topics to be covered	Ref	Teaching Method	Date covered	Remarks
		Boolean Algebra & Minimization Techniques				
1.	6.7.20	Introduction and brief of analog and digital systems	1,A	Online	6.7.20	
2.	7.7.20	Number systems and their conversions	1,A	Online	7.7.20	
3.	9.7.20	Binary codes applications and usefulness	1	Online	9.7.20	
4.	10.7.20	Self-complementing codes, Cyclic codes, 8-4-2-1 BCD code	1	Online	10.7.20	
5.	14.7.20	Excess-3 code, Gray code	1	Online	14.7.20	
6.	16.7.20	Binary to Gray and Gray to binary code conversion	1,B	Online	16.7.20	
7.	17.7.20	7-bit Hamming code, ASCII code, EBCDIC code	1,B	Online	17.7.20	
8.	21.7.20	Realization of Boolean Expressions: Reduction of Boolean expressions using laws	1,A	Online	21.7.20	
9.	23.7.20	Theorems and axioms of Boolean Algebra	1,A	Online	23.7.20	
10.	24.7.20	Boolean expressions and logic diagram	1,A	Online	24.7.20	
11.	28.7.20	Converting AND/OR/Invert logic toNAND/NOR logic	1,A	Online	28.7.20	
12.	30.7.20	Converting AND/OR/Invert logic toNAND/NOR logic	1,A	Online	30.7.20	
13.	31.7.20	SOP Forms and their Realization.	1	Online	31.7.20	
14.	4.08.20	POS Forms and their Realization.	1	Online	4.08.20	

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	
	Sub Code & Name : ***B028313(028) Digital System Design***	**LP –** ***B028313(028)*** **Rev. No:01 Date: 01.07.20**
	Unit:II ***Branch: E T & T*** ***Semester: III***	**Page 02 of 06**

Unit II: Combinational Circuit: MSI devices like Adder & Subtractor: Half and Full Adders, Half and Full Subtractor, Serial and Parallel Adders, BCD Adder, Comparators, Decoder: 3-Line to 8-Line Decoder, 8-4-2-1 BCD to Decimal Decoder, BCD to Seven Segment Decoder; Encoder: Octal to Binary and Decimal to BCD Encoder; Multiplexers: 2- Input Multiplexer,4-Input Multiplexer,16-Input Multiplexer; demultiplexers: 1-Line to 4-Line & 1-Line to 8- Line Demultiplexer; Applications of Multiplexers.

Objective: The students must acquire a thorough knowledge to develop Boolean expressions in SOP and POS forms and various minimization techniques using K-map and tabulation method.

LECT. No	Date planned	Topics to be covered	Ref	Teaching Method	Date covered	Remarks
		Combinational Circuit				
15	6.08.20	MSI devices like Adder & Subtractor	1,B	Online	6.08.20	
16	7.08.20	Half and Full Adders, Half and Full Subtractor	1,B,A	Online	7.08.20	
17	11.08.20	Serial and Parallel Adders, BCD Adder	1,A,B	Online	11.08.20	
18	13.08.20	Comparators, Decoder: 3-Line to 8-Line Decoder	1,A,B	Online	13.08.20	
19	14.08.20	3-Line to 8-Line Decoder	1	Online	14.08.20	
20	18.08.20	BCD to Seven Segment Decoder	1,B	Online	18.08.20	
21	20.08.20	Encoder: Octal to Binary and Decimal to BCD Encoder	1,B	Online	20.08.20	
22	21.08.20	2- Input Multiplexer,4-Input Multiplexer	1,A	Online	21.08.20	
23	25.08.20	16-Input Multiplexer; demultiplexers	1,A	Online	25.08.20	
24	27.08.20	1-Line to 4-Line & 1-Line to 8- Line Demultiplexer	1,A	Online	27.08.20	
25	29.08.20	Applications of Multiplexers.	1	Online	29.08.20	
26	1.09.20	Programmed Array Logic (PAL), PAL, PROM	1	Online	1.09.20	Content beyond Syllabus

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	
	Sub Code & Name : B028313(028) Digital System Design	LP – *B028313(028)* Rev. No:01 Date: 01.07.20
	Unit:III *Branch: E T & T* *Semester: III*	Page 03 of 06

Unit III- Sequential Circuits: Building blocks of Flip-Flops like S-R latch , Gated S-R Latch; D Latch, Edge Triggered Flip-Flops: S-R, D, J-K and T Flips-Flops; Master-Slave J-K Flip-Flop; Shift registers: SISO, SIPO, PISO,PIPO, Bi-Directional Shift Registers, Universal Shift register; Counters: Asynchronous Counters: Design of Asynchronous Counters; Ripple Counters: Effects of Propagation Delay in Ripple Counters; Synchronous Counters: Design of Synchronous Counters, 3- bit Synchronous Up counter, 3-bit Synchronous Down Counter,3-bit Synchronous Up-down Counter, Design Of Synchronous BCD Counter.

Objective: The students must acquire a thorough knowledge of various combinational circuits that can be used and also designing of these circuits.

LECT. No	Date planned	Topics to be covered	Ref	Teaching Method	Date covered	Remarks
		Sequential Circuits				
27	1.09.20	Building blocks of Flip-Flops like S-R latch	1,2	Online	3.09.20	
28	3.09.20	Gated S-R Latch; D Latch, Edge Triggered Flip-Flops	1,2	Online	6.10.20	
29	6.10.20	S-R, D, J-K and T Flips-Flops	1,2,B	Online	8.10.20	
30	8.10.20	Master-Slave J-K Flip-Flop	1	Online	9.10.20	
31	9.10.20	Shift registers: SISO, SIPO, PISO,PIPO	1,A	Online	13.10.20	
32	13.10.20	Bi-Directional Shift Registers, Universal Shift register	1,A	Online	15.10.20	
33	15.10.20	Counters: Asynchronous Counters: Design of Asynchronous Counters;	1,A	Online	16.10.20	
34	16.10.20	Ripple Counters: Effects of Propagation Delayin Ripple Counters	1,B	Online	20.10.20	
35	20.10.20	Synchronous Counters: Design of Synchronous Counters	1,B	Online	22.10.20	
36	22.10.20	3-bit Synchronous Up counter, 3-bit Synchronous Down Counter	1,B	Online	27.10.20	
37	27.10.20	3-bit Synchronous Up-down Counter	1,A	Online	29.10.20	
38	29.10.20	Design Of Synchronous BCD Counter.	1,A	Online	4.11.20	
39	4.11.20	Design of synchronous counters, Ringcounter, Jhonson counter	1,A	Online	5.11.20	Content beyond Syllabus

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	
	Sub Code & Name : ***B028313(028) Digital System Design***	**LP –** ***B028313(028)*** **Rev. No:01 Date: 01.07.20**
	Unit:IV ***Branch: E T & T*** ***Semester: III***	**Page 04 of 06**

Unit IV: **Finite State Machine:** Design of synchronous Finite state machine, Algorithmic State Machines charts. Designing synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator, Clock generation.

Objective: The students must be able to acquire knowledge of sequential circuit and must be able todesign sequential circuits for various applications.

LECT. No	Date planned	Topics to be covered	Ref	Teaching Method	Date covered	Remarks
		Finite State Machine				
40	5.11.20	Design of synchronous Finite state machine	1,B,C	Online	6.11.20	
41	6.11.20	Algorithmic State Machines charts	1,B,C	Online	11.11.20	
42	11.11.20	Designing synchronous circuits	1,b,C	Online	17.11.20	
43	17.11.20	Pulse train generator	1,B,C	Online	19.11.20	
44	19.11.20	Pseudo Random Binary Sequence generator	1,B,C	Online	23.11.20	
45	23.11.20	Clock generation	1,A	Online	23.11.20	

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	
	Sub Code & Name : ***B028313(028) Digital System Design***	**LP –** ***B028313(028)*** **Rev. No:01 Date: 01.07.20**
	Unit:V ***Branch: E T & T*** ***Semester: III***	**Page 05 of 06**

Unit V: **DIGITAL LOGIC FAMILIES:** Introduction; Simple Diode Gating and Transistor Inverter; Digital IC Specification Terminology; Logic Families: TTL: Open collector gates, TTL subfamilies; IIL; ECL; MOS Logic; CMOS Logic; Dynamic MOS Logic; Interfacing: TTL to ECL, ECL to TTL, TTL to CMOS, CMOS to TTL; Comparison Among Various Logic Families, Manufacturer's Specification.

Objective: Students must be able to design a timer circuit and should learn the basics of working of voltage regulators.

LECT. No	Date planned	Topics to be covered	Ref	Teaching Method	Date covered	Remarks
		DIGITAL LOGIC FAMILIES				
46	26.11.20	Simple Diode Gating and Transistor Inverter	1,2	Online	24.11.20	
47	27.11.20	Basic Concepts of RTL and DTL	1,2	Online	24.11.20	
48	28.11.20	Open collector gates, TTL subfamilies, IIL, ECL; MOS Logic: CMOS Logic	1,2	Online	26.11.20	
49	29.11.20	Interfacing: TTL to ECL, ECL to TTL, TTL to CMOS	1,2	Online	27.11.20	
50	30.11.20	CMOS to TTL, Comparison among various logic families, Manufacturer's specification.	1,2	Online	28.11.20	

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	
ASPIRE TO EXCEL	*Sub Code & Name :* ***B028313(028) Digital System Design***	**LP –** ***B028313(028)***
		Rev. No:01 Date:
	Branch: E T & T ***Semester: III***	**01.07.20**
		Page 06 of 06

Course Delivery Plan:

Week	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII	XIII	XIV
Unit	I	I	I	II	II	II	III	III	III	IV	IV	IV	V	V
	I	I	II	II	II	III	III	III	III	IV	IV	IV	V	V

References: Name of Text Books:

Text Book:

1. Fundamentals of Digital Circuits: A. Anand Kumar, PHI.(Unit – I to V)
2. Digital Electronics-Principles and Integrated Circuits, A.K. Maini, 1st Edition, Wiley India.

Reference Books:

A. Digital Fundamentals: Floyd & Jain: Pearson Education.
B. Digital Electronics: A. P. Malvino: Tata McGraw Hill.
C. Digital Circuits & Logic Design–LEE, PHI.

	Prepared by	Approved by
Signature		
Name	Mrs. KiranDewangan	Dr. Naveen Kumar Dewangan
Designation	Asst. Prof. of Electronics & Telecommunication	HOD- Deptt. of Electronics & Telecommunication
Date	06.07.20	

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	LP – 328744 (28)
ASPIRE TO EXCEL	*Sub Code & Name : 328744 (28, RADAR and NAVIGATIONAL AIDS*	Date: 01.07.20
	Unit: II *Branch: E T & T* *Semester: VII*	Page 02 of 06
	Section A+B	

Unit syllabus:

MTI And Pulse Doppler Radar: Introduction to Doppler and MTI Radar, delay line cancellers, staggered PRF. Range gated Doppler filter, limitations to MTI performance. Tracking with Radar, Monopulse Tracking, Conical Scan and Sequential Lobing, Limitations to Tracking Accuracy, Low Angle Tracking, Tracking in range, Comparison of Trackers.

Objective:

In this Unit Students will acquire basic knowledge about different types of Radar, imitation to their performance, tracking with radar their limitation & Comparisons of Trackers.

Session No	Day/Order	Topics to be covered	Date Planned	Date Covered
		MTI And Pulse Doppler Radar:		
14	3	Introduction to Doppler and MTI Radar	07/08/20	07/08/20
15	3	delay line cancellers	11/08/20	11/08/20
16	3	staggered PRF	11/08/20	11/08/20
17	3	Range gated Doppler filter	14/08/20	14/08/20
18	3	limitations to MTI performance	14/08/20, 18/08/20	14/08/20, 18/08/20
19	3	Tracking with Radar	19/08/20	19/08/20
20	3	Monopulse Tracking	19/08/20	19/08/20
21	3	Conical Scan and Sequential Lobing	21/08/20	19/08/20, 21/08/20
22	3	Limitations to Tracking Accuracy	21/08/20	21/08/20
23	3	Low Angle Tracking	25/08/20	21/08/20
24	3	Tracking in range	25/08/20	25/08/20
25	3	Comparison of Trackers	26/08/20	25/08/20
26	3	Numerical	26/08/20	26/08/20
27	3	Summary and Tutorials	26/08/20	26/08/20
28	3	Discussion of Previous Question Papers	26/08/20	26/08/20

Name of Faculty: Mrs .KiranDewangan

	LESSON PLAN	LP – 328744 (28)
ASPIRE TO EXCEL	*Sub Code & Name : 328744 (28),RADAR and NAVIGATIONAL AIDS*	Date: 01.07.20
	Unit: III *Branch: E T & T* *Semester: VII*	Page 03 of 06
	Section A+B	

Unit syllabus:

Propagation of Radar Waves: Forward Scattering from a Flat Earth, Scattering from Round Earth' s Surface, Atmospheric Refraction – Standard Propagation, Non-Standard Propagation, Diffraction, Attenuation by Atmospheric Gases, External or Environmental Noise, Other Propagation Effects.

Objective:

In this Unit Students will acquire basic knowledge about multistage amplifiers and various stages of Power Amplifiers

Session No	Day/Order	Topics to be covered	Date Planned	Date Covered
		Propagation of Radar Waves:		
29	5	Forward Scattering from a Flat Earth	23/09/20	21/10
30	5	Scattering from Round Earth's Surface	25/09/20, 29/09/20	21/10
31	5	Atmospheric Refraction – Standard Propagation	30/09/20	21/10
32	5	Non-Standard Propagation	06/10/20	21/10
33	5	Diffraction	07/10/20	27/10
34	5	Attenuation by Atmospheric Gases	09/10/20	27/10
35	5	External or Environmental Noise	09/10/20	27/10
36	5	Other Propagation Effects	13/10/20	27/10
37	5	Numerical	14/10/20	21/10
38	5	Numerical	16/10/20	27/10
39	5	Summary and Tutorials	20/10/20	27/10
40	5	Discussion of Previous Question Papers	20/10/20	27/10

Name of Faculty: Ekta Tamrakar

	LESSON PLAN	LP – 328744 (28) Date: 1.07.20 Page 06of 06
	Sub Code & Name : 328744 (28) RADAR & NAVIGATIONAL AIDS *Unit: I-5* *Branch: E T & T* *Semester: VII* *Section A+B*	

Course Delivery Plan:

Week	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII	XIII
Unit	1	1	1	2	2,3	3	3	3,4					

References:

Text Books:

1. Introduction to Radar Systems by M.I Skolnik, TMH Pub. Co.
2. Microwave Radar and Navigational Aids by A.K. Sen and A.B. Bhattacharya, Khanna Publisher.

Reference Books:

1. Radar: Principles, Technology, Applications by Edde, Pearson Ecuation Pub.
2. Elements of Electronic Navigation by Nagaraj, TMH Pub.

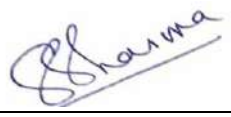
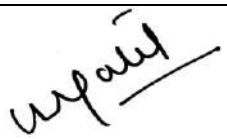
	Prepared by	Approved by
Signature		
Name	**Mrs. Ekta Tamrakar**	**Dr. N.K. Dewangan**
Designation	**Asst. Professor, Deptt. of Electronics & Telecommunication**	**HOD- Deptt of Electronics & Telecommunication**
Date	**01.07.20**	

Radar and Navigational Aids

Program: MBA	**Semester&Section: II Semester, Section-B**		
Course: Management	**Session: Jan-June 2022**		
Teacher: Dr. Sheetal Sharma	**L =3**	**T =1**	**P =0**
Subject: Financial Management	**Course Code**	**251204MG**	

Consumer Electronics

Lecture Hour	Lesson Particulars	Planned Date	Actual Date	Instructional Media *
UNIT-I:Introduction to Financial Management[8 Hrs]				
1.	Introduction to financial management – Nature & objectives of FM	23-3-22	23-3-22	1 &2
2.	Profit maximization v/s Wealth maximization Finance Function	30-3-22	30-3-22	1 &2
3.	Time value of money (discounting and compounding techniques)	31-3-22	31-3-22	1 &2
4.	Problems on Compounding & Discounting	4-4-22	4-4-22	1 & 2
5.	Long term & Short term sources of finance , Capital Markets introduction	6-4-22	6-4-22	1 & 2
UNIT-II: Financing Decision:[7Hrs]				
6.	Cost of raising Capital, Weighted average cost of capital	23-3-22	23-3-22	1 &2
7.	Problems & Solutions on cost of capital	30-3-22	30-3-22	1 &2
8.	Capital structure	31-3-22	31-3-22	1 &2
9.	Numericals on Capital Structure	4-4-22	4-4-22	1 &2
10.	Leverage	6-4-22	6-4-22	1 &2
11.	Numericals on Leverage	23-3-22	23-3-22	1 &2
UNIT-III: Investment Decision[7 Hrs]				
12.	Budget & its types	21-6-22	21-6-22	1 &2
13.	Budgetary Control	22-6-22	22-6-22	1 &2
14.	Capital Budgeting – formulas	22-6-22	22-6-22	1 &2
15.	Capital Budgeting Techniques	23-6-22	23-6-22	1 &2
16.	Problems on capital budgeting	4-7-22	4-7-22	1 &2
17.	Zero based budgeting and its importance	6-7-22	6-7-22	1 &2
UNIT-IV: Working Capital Decision[7 Hrs]				
18.	Management of working capital, Concept Need and factors influencing WC	7-7-22	7-7-22	1 &2
19.	Estimation of Working Capital	8-7-22	8-7-22	1 &2
20.	Inventory Management	11-7-22	11-7-22	1 &2
21.	Receivables Management	11-7-22	11-7-22	1 &2
22.	Management of Cash – Models	13-7-22	13-7-22	1 &2
UNIT-V: Dividend Decision[7 Hrs]				
23.	Dividend Decision meaning	13-7-22	13-7-22	1 &2
24.	Types	14-7-22,(2)	14-7-22,(2)	1 &2
25.	Factors influencing dividend policy	16-7-22	16-7-22	1 &2
26.	Models of Dividend	16-7-22	16-7-22	1 &2
27.	Numericals on Models	18-7-22	18-7-22	1 &2
Remarks:				

	
Signature of Teacher	**Signature of the HOD**

***InstructionalMedia**		
1.Chalk & Talk	2.Digital Class Room	3.Computer Lab
4.Audio-Video conferencing	5.Real Object	6. Charts
Any Other (Please specify)		

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Unit syllabus:

Fundamentals of Television: Elements of Television system, Scanning Process, Scanning Methods and Aspect Ratio, Persistence of Vision and Flicker, Vertical Resolution, Picture Elements, Kell Factor, Horizontal Resolution and Video Bandwidth, Interlacing of Scanning Lines, Video Signals, Control Pulses, Composite Video Signal, TV Standards:625 Line System.

Objective:

In this Unit Students will acquire basic knowledge about monochrome television, Its parameters & TV Standards.

S No	Day/Order	Topics to be covered	Date Planned	Date Covered
		Fundamentals of Television:		
1	4	Elements of Television system	01/04/21	01/04/21
2	4	Scanning Process	01/04/21	01/04/21
3	4	Scanning Methods and Aspect Ratio,	01/04/21	01/04/21
4	4	Persistence of Vision and Flicker	05/04/21	05/04/21
5	4	Vertical Resolution	05/04/21	05/04/21
6	4	Picture Elements, Kell Factor	05/04/21	05/04/21
7	4	Horizontal Resolution and Video Bandwidth	07/04/21	06/04/21
8	4	Interlacing of Scanning Lines	07/04/21	06/04/21
9	4	Video Signals	07/04/21	06/04/21
10	4	Control Pulses, Composite Video Signal, TV Standards:625 Line System	08/04/21	07/04/21
11	4	Numerical	08/04/21	07/04/21
12	4	Summary and Tutorial	08/04/21	07/04/21
13	4	Discussion of Previous Question Papers	08/04/21	07/04/21

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Unit syllabus:

Color TV: Introduction, Color Spectrum, Compatibility Consideration, Color TV Signal, Luminance Signal, Chrominance Signal, Luminance and Chrominance ,Recombination to Natural Color Voltages, Interleaving Process, Color Subcarrier Frequency, Phase Errors, Composite Color Signal, High Definition TV, Digital TV.

Objective:

In this Unit Students will acquire basic knowledge about Color TV Signal, High Definition TV, Digital TV & its various properties.

S No	Day/Order	Topics to be covered	Date Planned	Date covered
		Color TV:		
14	5	Introduction,	12/04/21	08/04/21
15	5	Color Spectrum, Compatibility Consideration	12/04/21	08/04/21
16	5	Color TV Signal	12/04/21	08/04/21
17	5	Luminance Signal	15/04/21	12/04/21
18	5	Chrominance Signal	15/04/21	12/04/21
19	5	Luminance and Chrominance	15/04/21	15/04/21
20	5	Recombination to Natural Color Voltages	19/04/21	15/04/21
21	5	Interleaving Process,	19/04/21	19/04/21
22	5	Color Subcarrier Frequency	28/04/21	19/04/21
23	5	Phase Errors	28/04/21	22/04/21, 26/04/21
24	5	Composite Color Signal,	29/04/21	28/04/21
25	5	High Definition TV, Digital TV.	29/04/21	28/04/21, 29/04/21
26	5	Numerical	03/05/21	29/04/21
27	5	Summary and Tutorials	03/05/21	29/04/21, 03/05/21
28	5	Discussion of Previous Question Papers	03/05/21	29/04/21, 03/05/21

Unit syllabus:

Microphone and Optical Recording: Microphone: Characteristics of Microphones, Construction and working Principles of Microphones, Carbon Microphone, Dynamic Microphone, Capacitor Microphone, Tie Clip Microphone ,Wireless Microphone

Optical Recording of Audio Signal: Disc, Processing of Audio signal, Readout from the Disc, Reconstruction of the Audio Signal

Objective:

In this Unit Students will acquire basic knowledge about multistage amplifiers and various stages of Power Amplifiers

S No	Day/Order	Topics to be covered	Date Planned	Date covered
		Microphone and Optical Recording: Microphone:		
29	1	Characteristics of Microphones	01/03/21	01/03/21
30	1	Construction and working Principles of Microphones	01/03/21	01/03/21
31	1	Carbon Microphone Dynamic Microphone,	03/03/21	03/03/21
32	1	Capacitor Microphone Tie Clip Microphone	03/03/21	03/03/21
33	1	Wireless Microphone	05/03/21	05/03/21
34	1	**Optical Recording of Audio Signal:** Disc, Processing of Audio signal	05/03/21	05/03/21
35	1	Readout from the Disc	08/03/21	08/03/21
36	1	Reconstruction of the Audio Signal	08/03/21	08/03/21
37	1	Numerical	08/03/21	08/03/21
38	1	Numerical	08/03/21	08/03/21
39	1	Summary and Tutorials	08/03/21	08/03/21
40	1	Discussion of Previous Question Papers	08/03/21	08/03/21

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Unit syllabus:

Public Address System: Loudspeaker: Ideal Loudspeaker, Basic Loudspeaker, Capacitor Loudspeaker, permanent Magnet loudspeaker, Voice coil ,Loudspeaker Impedance, Acoustic Impedance and Resonance, Woofers ,Horn Type Tweeters. **Loudspeaker System:** Horns, Indoor Acoustics.

Public Address system: Introduction to PA system, Planning a PA System, Speaker Matching System, PA System Characteristics,

Objective:

In this Unit Students will acquire basic knowledge about Basic Public Address System and its application.

S No	Day/Order	Topics to be covered	Date Planned	Date Covered
		Public Address System: Loudspeaker:		
41	2	Ideal Loudspeaker, Basic Loudspeaker,	10/03/21	10/03/21
42	2	Capacitor Loudspeaker, permanent Magnet loudspeaker,	10/03/21	10/03/21
43	2	Voice coil ,Loudspeaker Impedance,	15/03/21	15/03/21
44	2	Acoustic Impedance and Resonance,	15/03/21	15/03/21
45	2	Woofers ,Horn Type Tweeters	17/03/21	17/03/21
46	2	**Loudspeaker System:** Horns, Indoor Acoustics.	17/03/21	17/03/21
47	2	**Public Address system:** Introduction to PA system	17/03/21	17/03/21
	2	Planning a PA System	18/03/21	18/03/21
	2	Speaker Matching System, PA System Characteristics,	18/03/21	18/03/21
48	2	Summary and Tutorial	18/03/21	18/03/21
49	2	Discussion of Previous Question Papers	18/03/21	18/03/21

Unit syllabus:

Electronics in Home Appliances and Automobiles: Microwave Oven: Block diagram, LCD timer with Alarm, Single Chip Controller, **Washing Machine:** Electronic Controller for Washing Machine, Washing Machine Hardware, Washing Cycles-Hardware and Software Development, Fuzzy Logic Washing Machine. **Electronics in Automobiles:** In Car Computers: Applications, Electronic Ignition, Electronic Ignition Lock System ,Anti-Lock Braking System, Electronically Control Suspension, Instruments Panel Displays, Ultrasonic Car Safety Belt System, Air Bag System, Vehicle Proximity Detection System, Car navigation system.

Objective:

In this Unit Students will acquire basic knowledge about different types of Home Appliances and Automobiles.

S No	Day/Order	Topics to be covered	Date Planned	Date Covered
		Electronics in Home Appliances and Automobiles:		
50	3	**Microwave Oven:** Block diagram, LCD timer with Alarm,	22/03/21	18/03/21
51	3	Single Chip Controller	22/03/21	24/03/21
52	3	**Washing Machine:** Electronic Controller for Washing Machine,	24/03/21	24/03/21
53	3	Washing Machine Hardware	24/03/21	24/03/21
54	3	Washing Cycles-Hardware and Software Development,	24/03/21	25/03/21
55	3	Fuzzy Logic Washing Machine.	25/03/21	25/03/21
56	3	**Electronics in Automobiles:** In Car Computers: Applications, Electronic Ignition,	25/03/21	25/03/21
57	3	Electronic Ignition Lock System ,Anti-Lock Braking System	25/03/21	25/03/21
58	3	Electronically Control Suspension, Instruments Panel Displays	31/03/21	31/03/21
59	3	Ultrasonic Car Safety Belt System,	31/03/21	31/03/21
60	3	Air Bag System, Vehicle Proximity Detection System	31/03/21	31/03/21
61	3	Car navigation system	31/03/21	31/03/21

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ASPIRE TO EXCEL	*Sub Code & Name : 328832 (28) CONSUMER ELECTRONICS*	Date: 11.02.21
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Course Delivery Plan:

Week	I	II	III	IV	V	VI	VII	VIII	IX	X	XI	XII	XIII
Unit	1	1, 2	2	3	3	3	4	5	5	5	-	-	-

References:

Text Books:

1. Consumer Electronics by S.P. Bali, Pearson Publication
2. Color Television by S.P. Bali, McGraw Hill

Reference Books:

1. Monochrome and color TV by R.R.Gulati, 3rd Edition, New Age International
2. Basic TV and video systems by Benard Globb.
3. Audio and Video System by R.G. Gupta, 2nd Edition, McGraw Hill

	Prepared by	Approved by
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Date	11.02.21	~~11.02.21~~ 03.05.21

Consumer Electronics

2. Academic Calender 2016-2023: